

FIG. 1

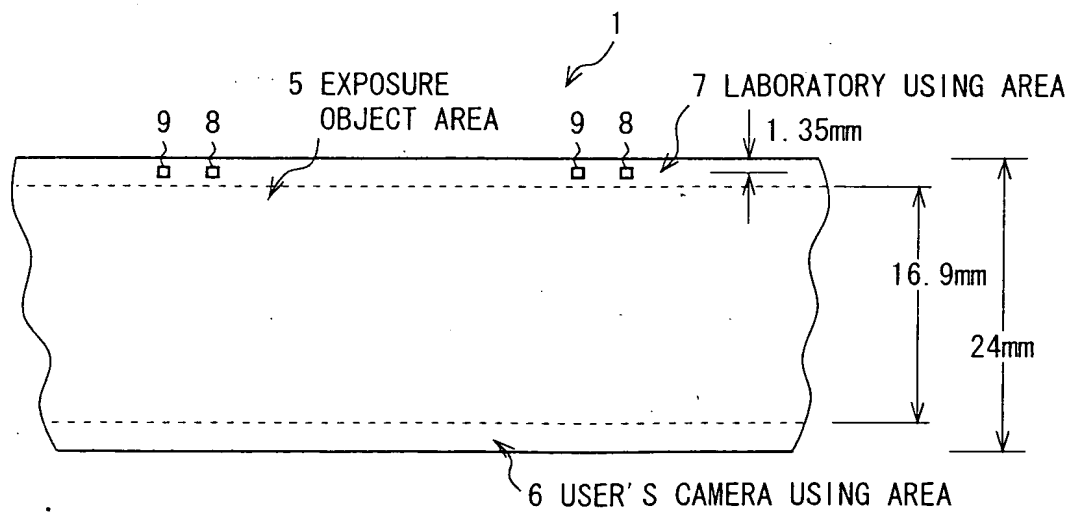


FIG. 2

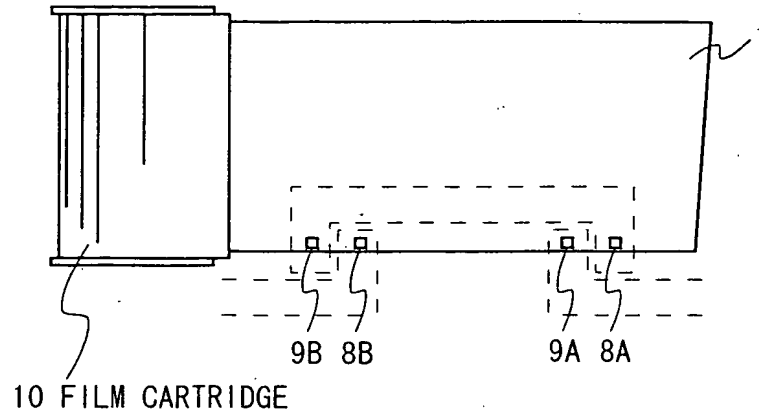


FIG. 3

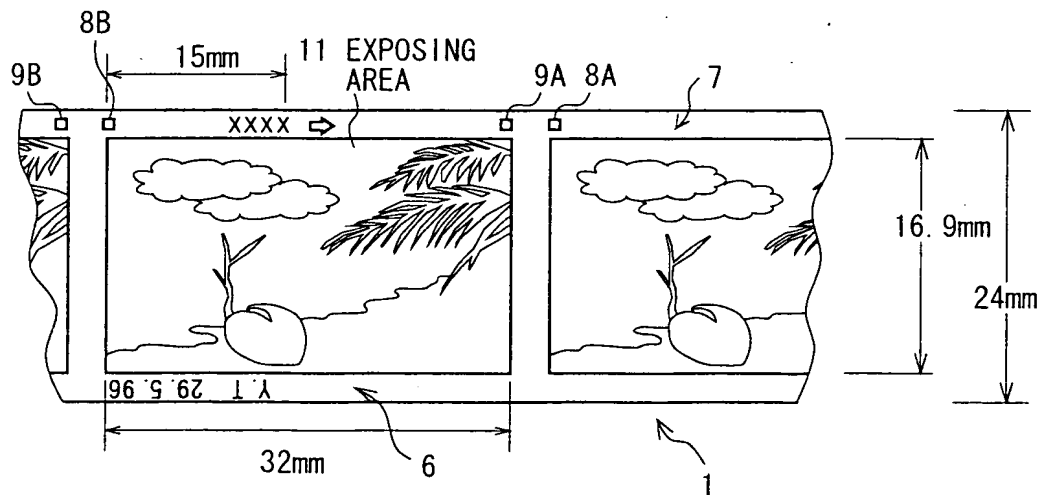


FIG. 4

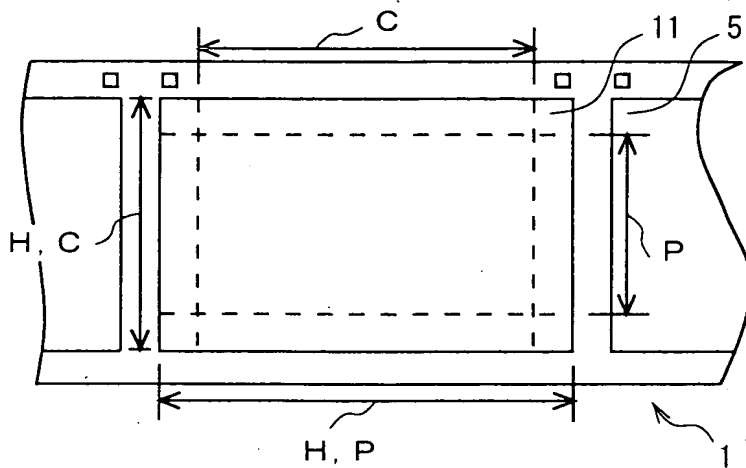


FIG. 5

FIG. 6A

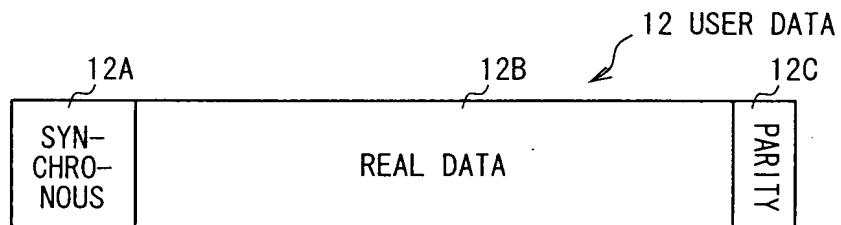
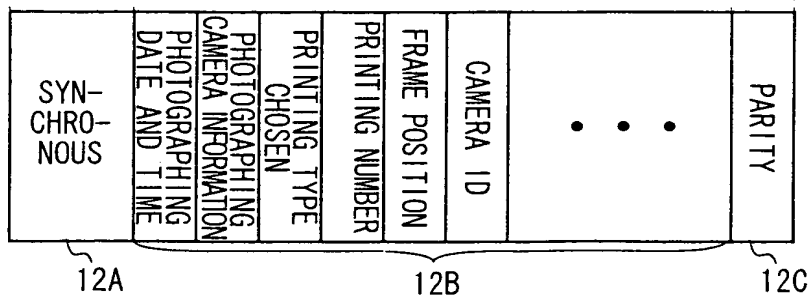


FIG. 6B



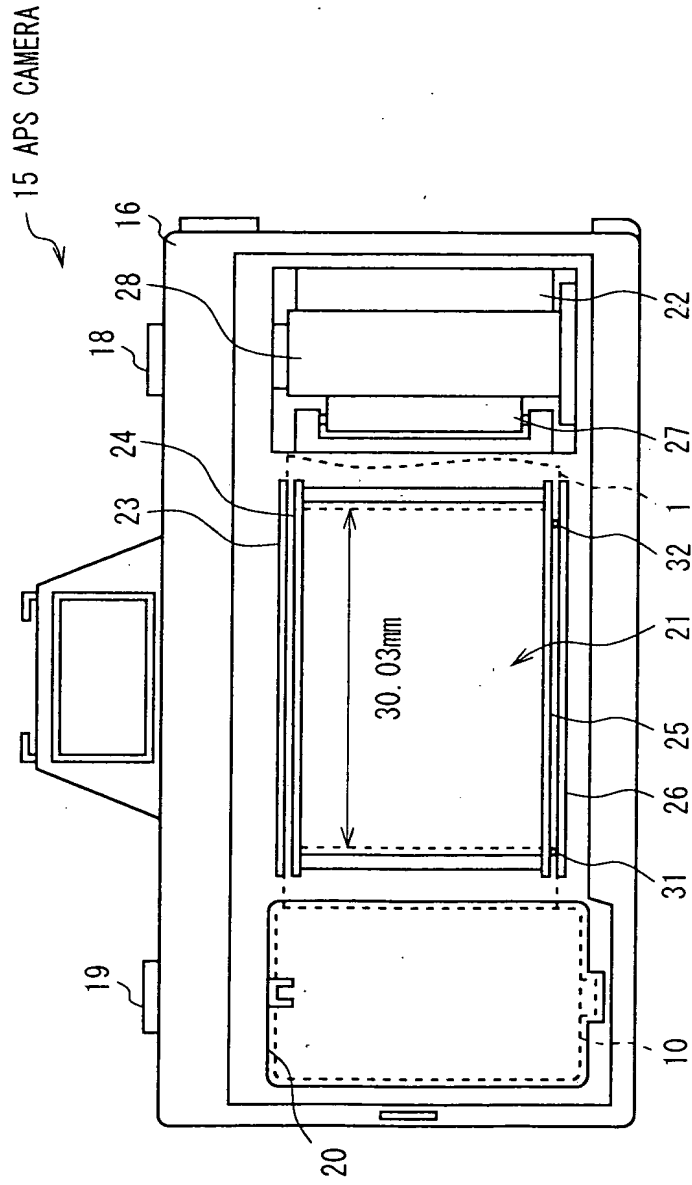


FIG. 7

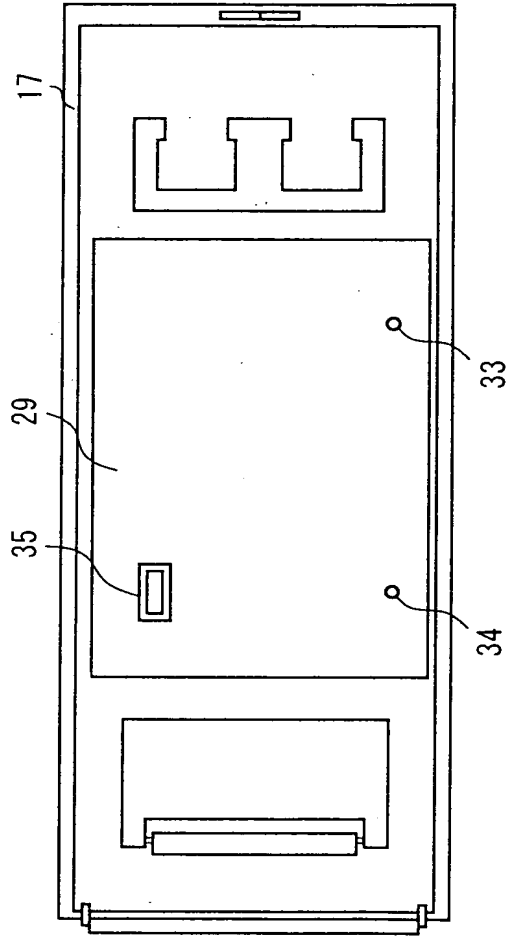


FIG. 8

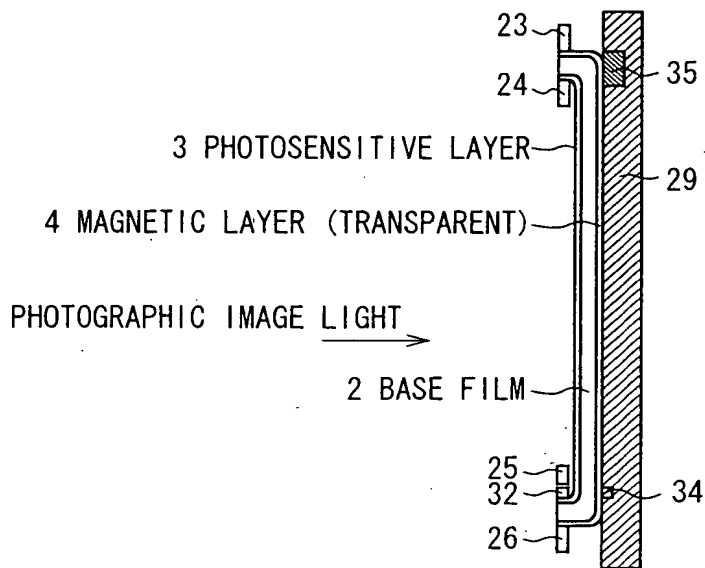
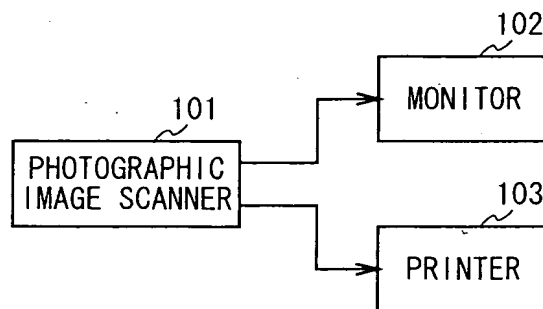


FIG.9



100 PHOTOGRAPHIC IMAGE PRINTING MACHINE

FIG.11

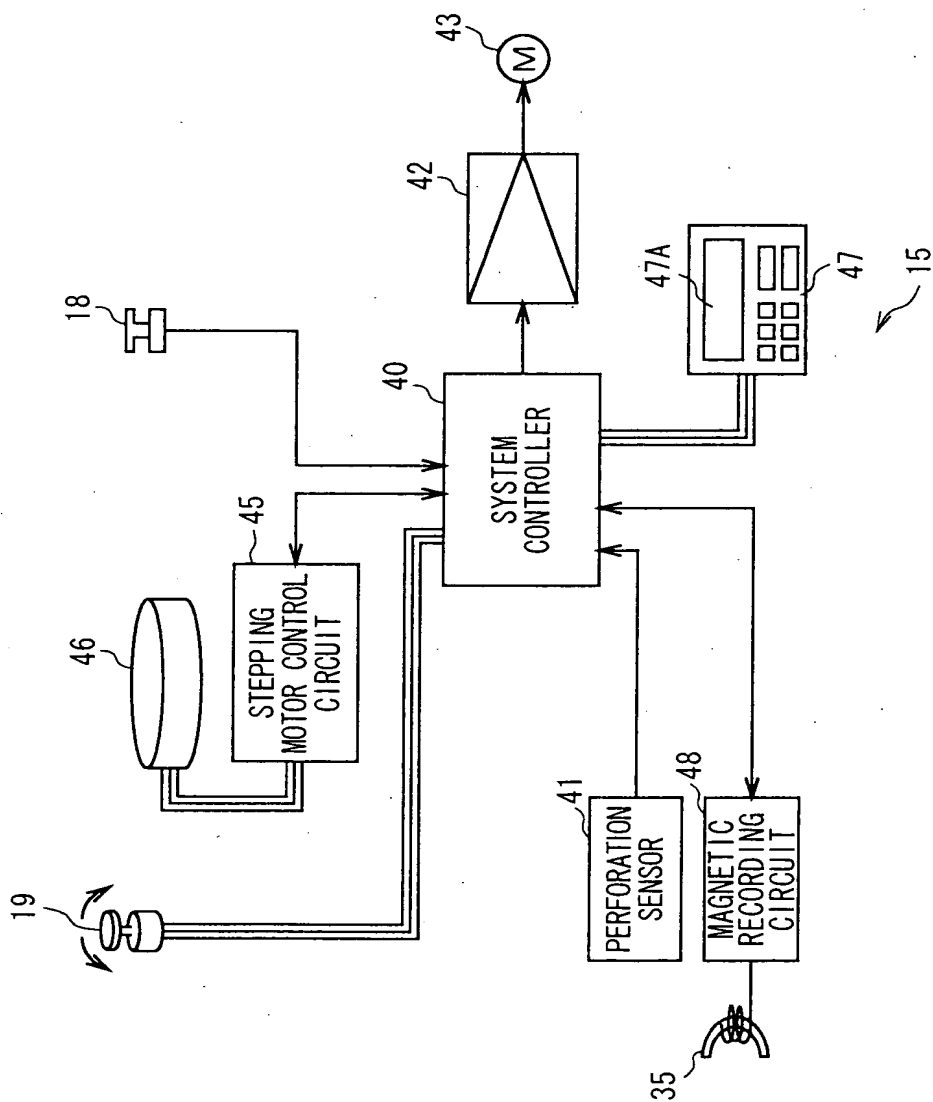


FIG.10

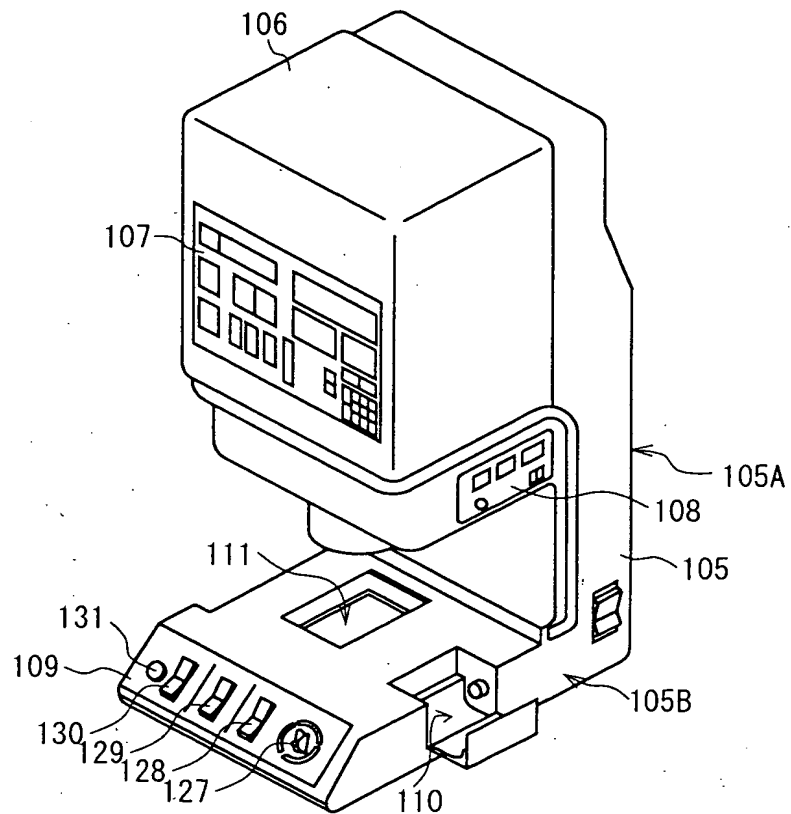


FIG.12



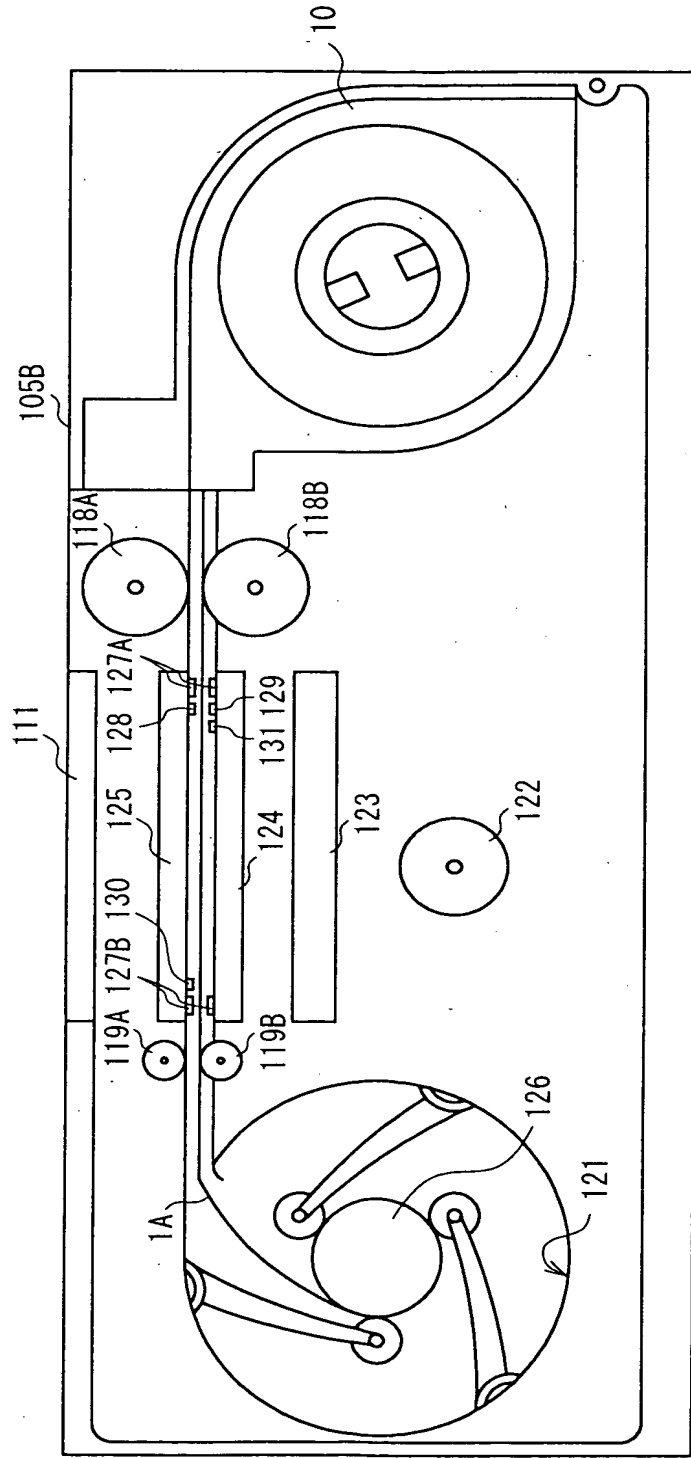


FIG.13

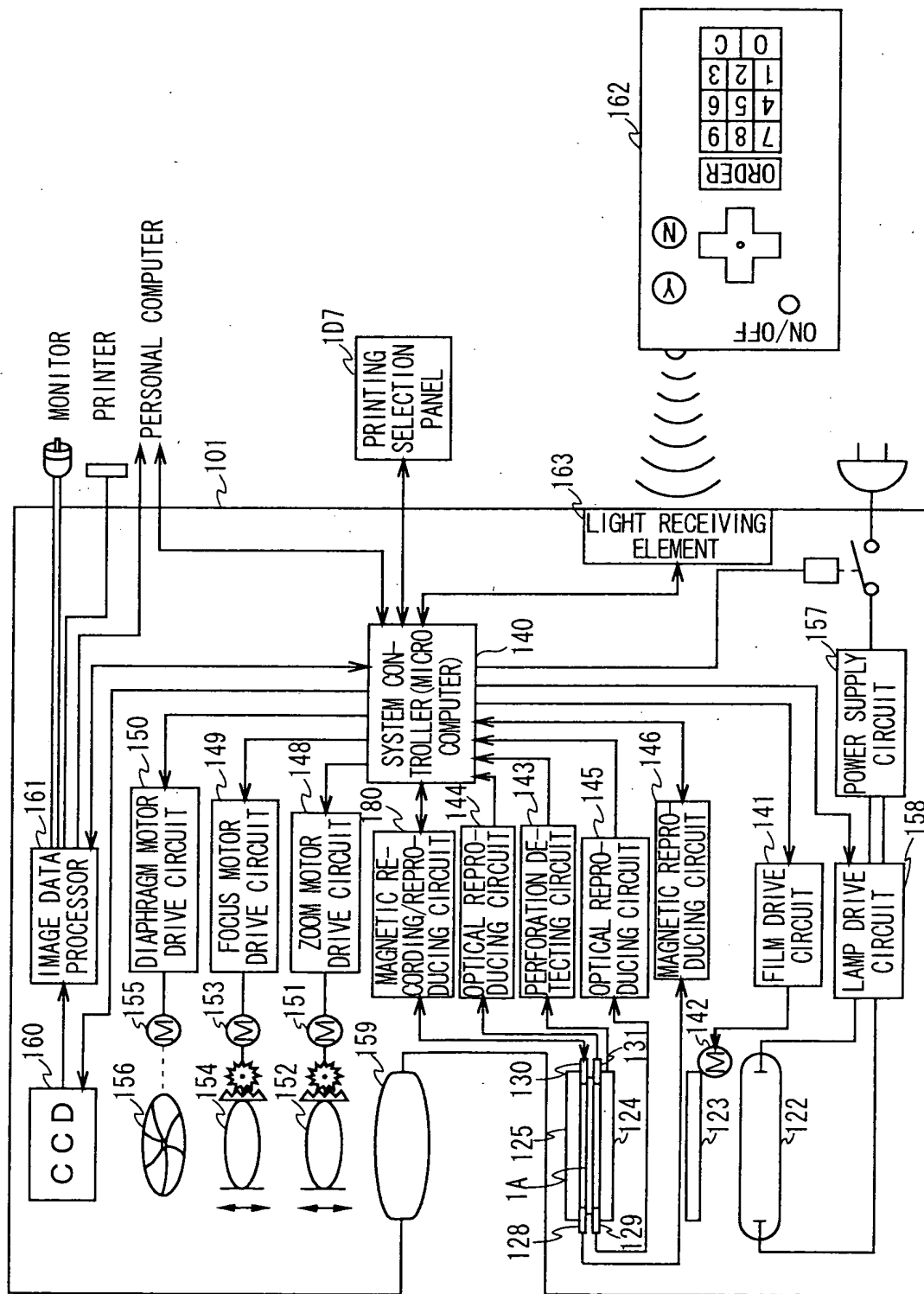


FIG.14

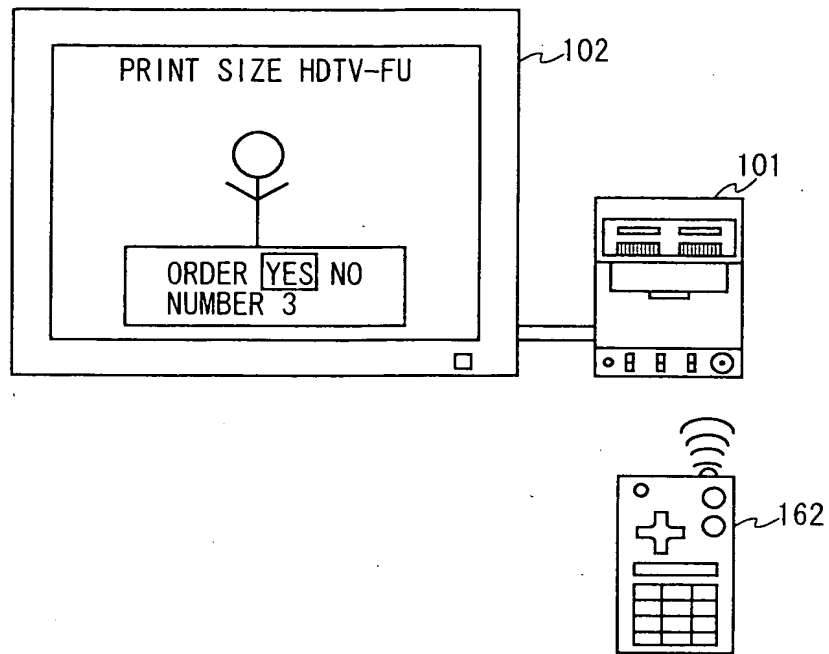


FIG.15

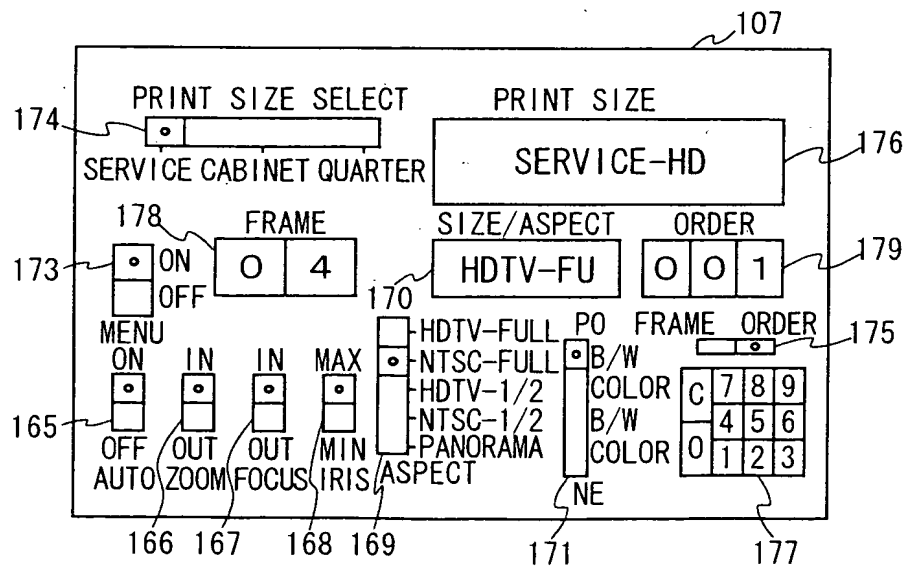


FIG.16

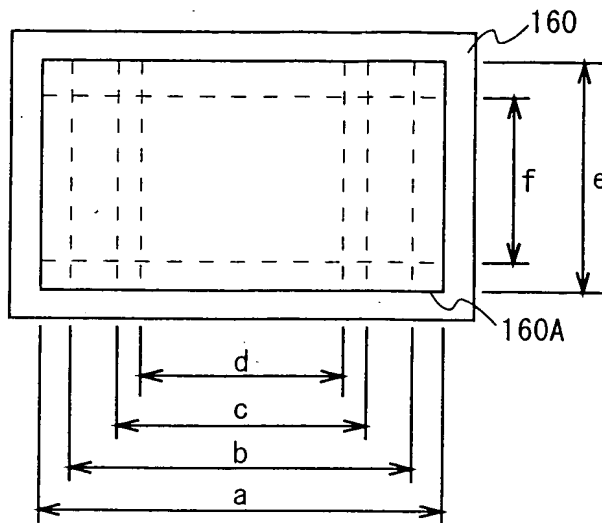


FIG. 17

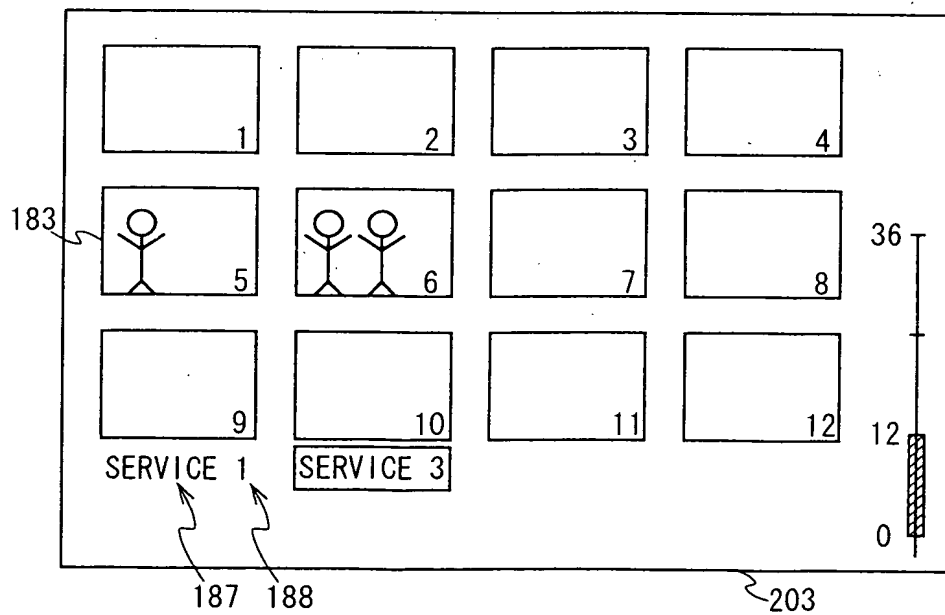


FIG. 21

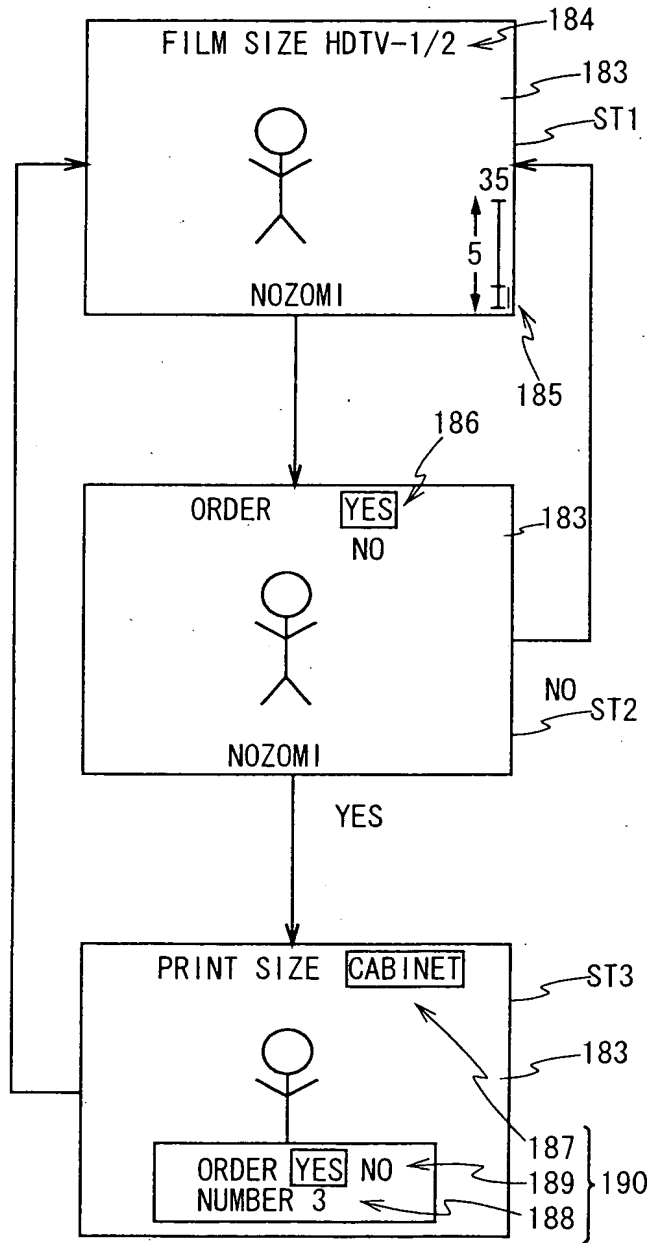


FIG.18

FIG. 19 is a block diagram of a system 161. The system 161 includes a CCD 160, a microprocessor 195, a memory 196, an image processor 197, a memory 199, an interface 200, a system controller 140, a monitor, and a printer. The CCD 160 is connected to the microprocessor 195 via a signal line S1. The microprocessor 195 is connected to the memory 196 via a data bus D1, D2. The microprocessor 195 is connected to the image processor 197 via a data bus D3~D6, D8~D15. The image processor 197 is connected to the memory 199 via a data bus D7. The image processor 197 is connected to the interface 200 via a data bus D8~D13, D15. The interface 200 is connected to the system controller 140 via a data bus D4~D6, D14. The interface 200 is connected to the monitor and printer via a data bus D11~D13, D15. The interface 200 is connected to the personal computer via a data bus D8~D10.

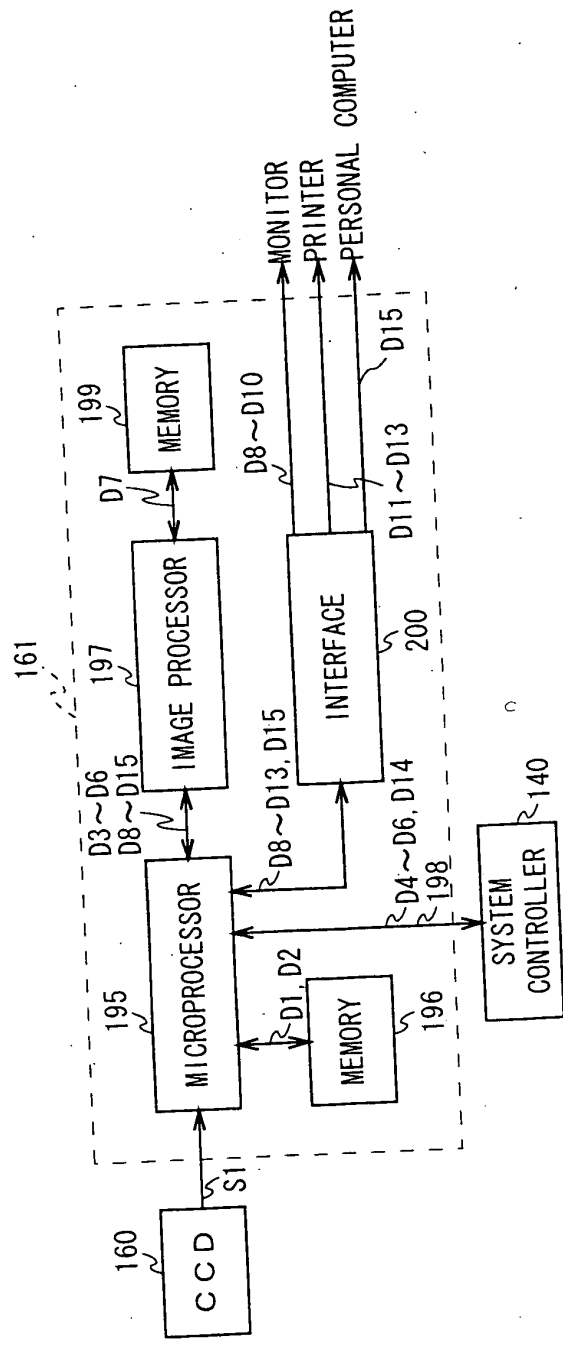


FIG.19

FIG.20A

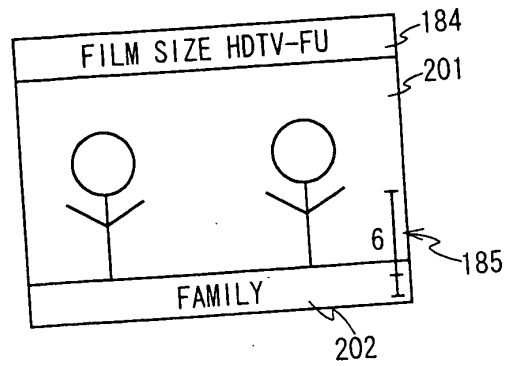


FIG.20B

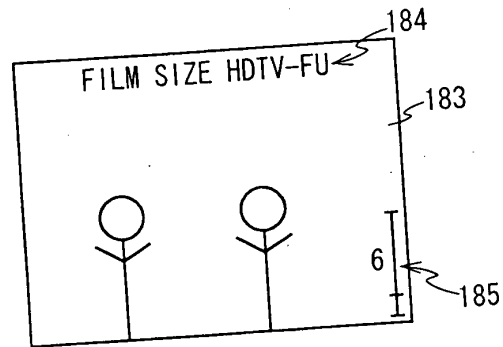


FIG.20C

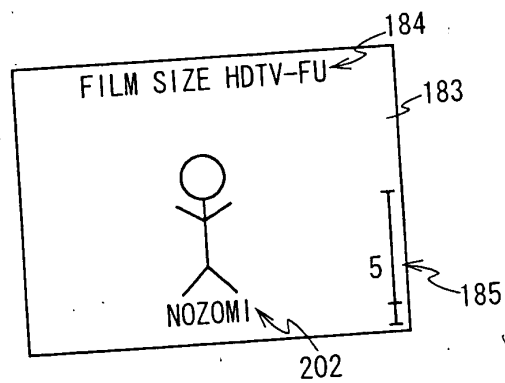
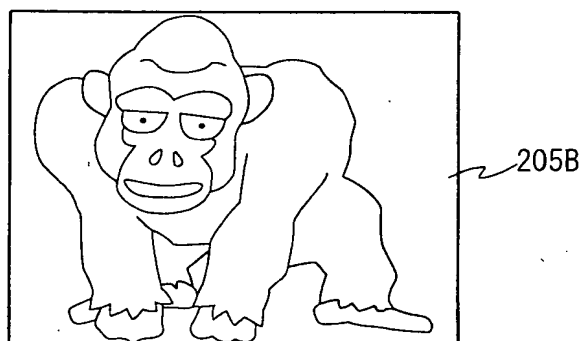


FIG.22A



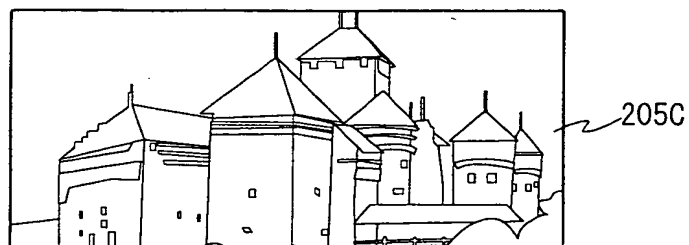
FIRST PRINTING TYPE

FIG. 22B



SECOND PRINTING TYPE

FIG.22C



THIRD PRINTING TYPE



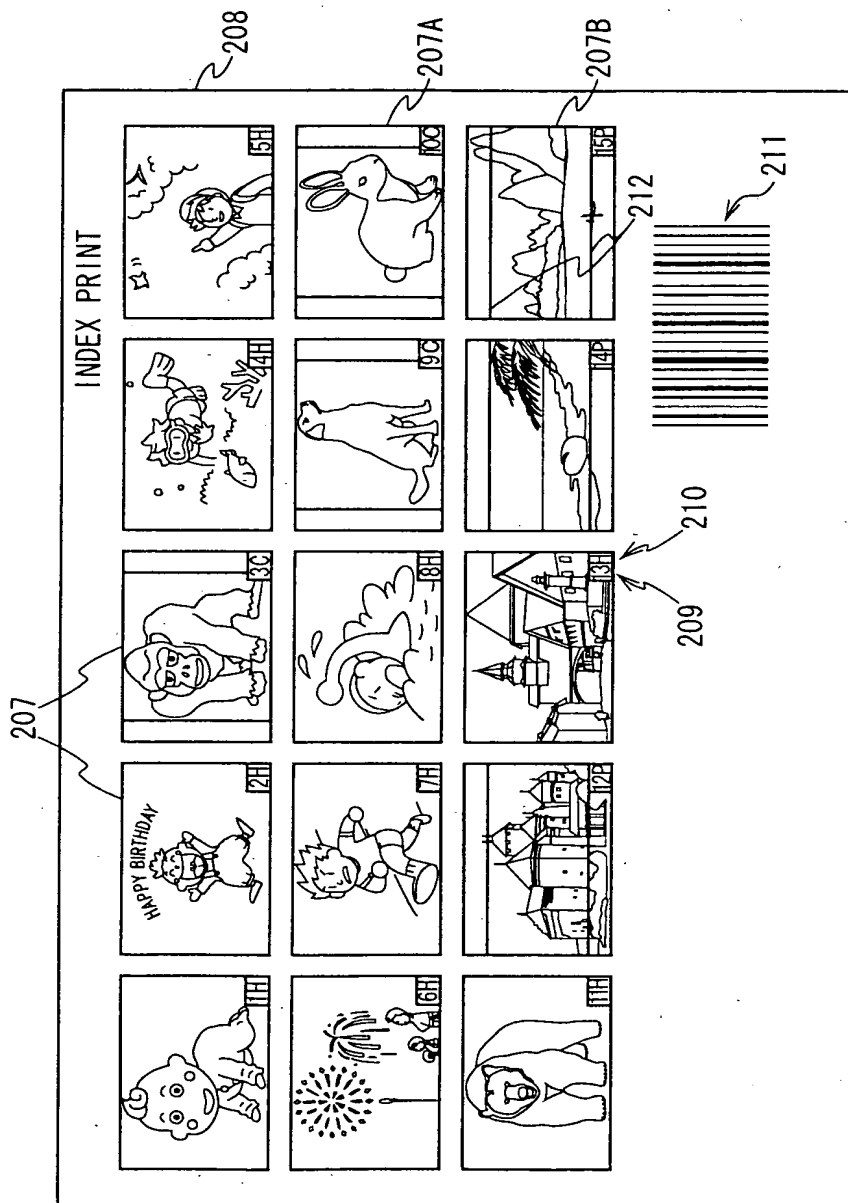


FIG.23

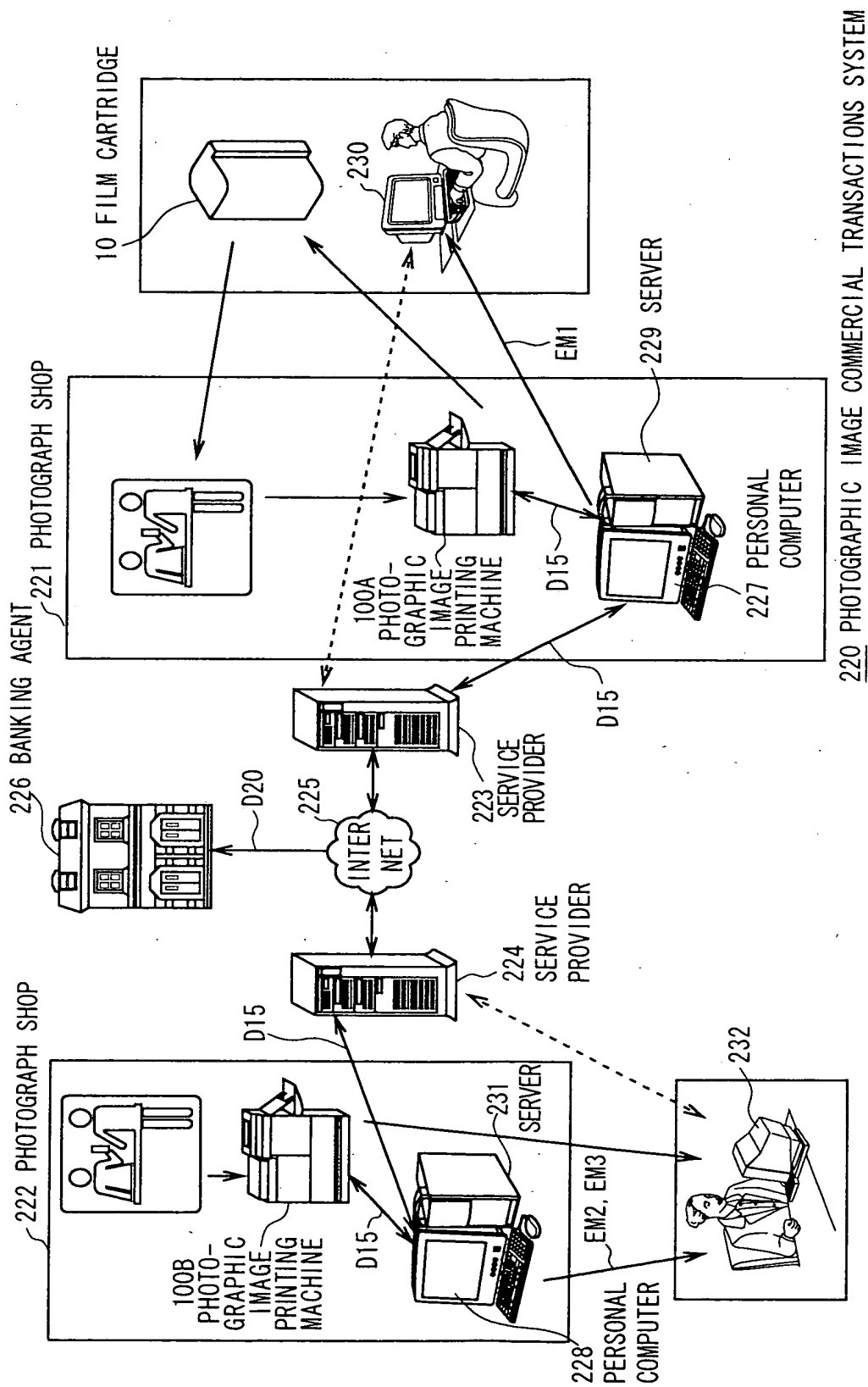


FIG.24

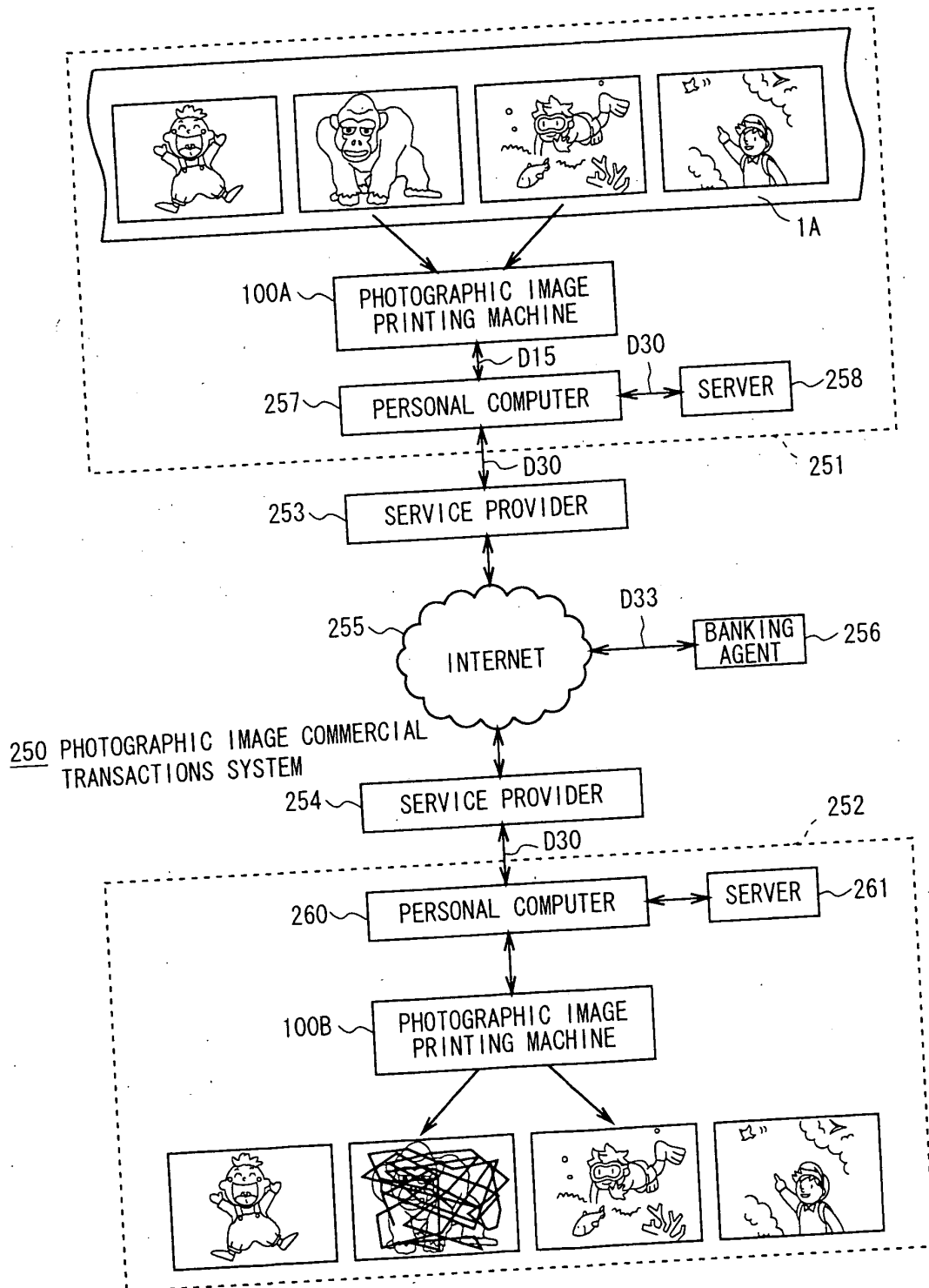


FIG.25

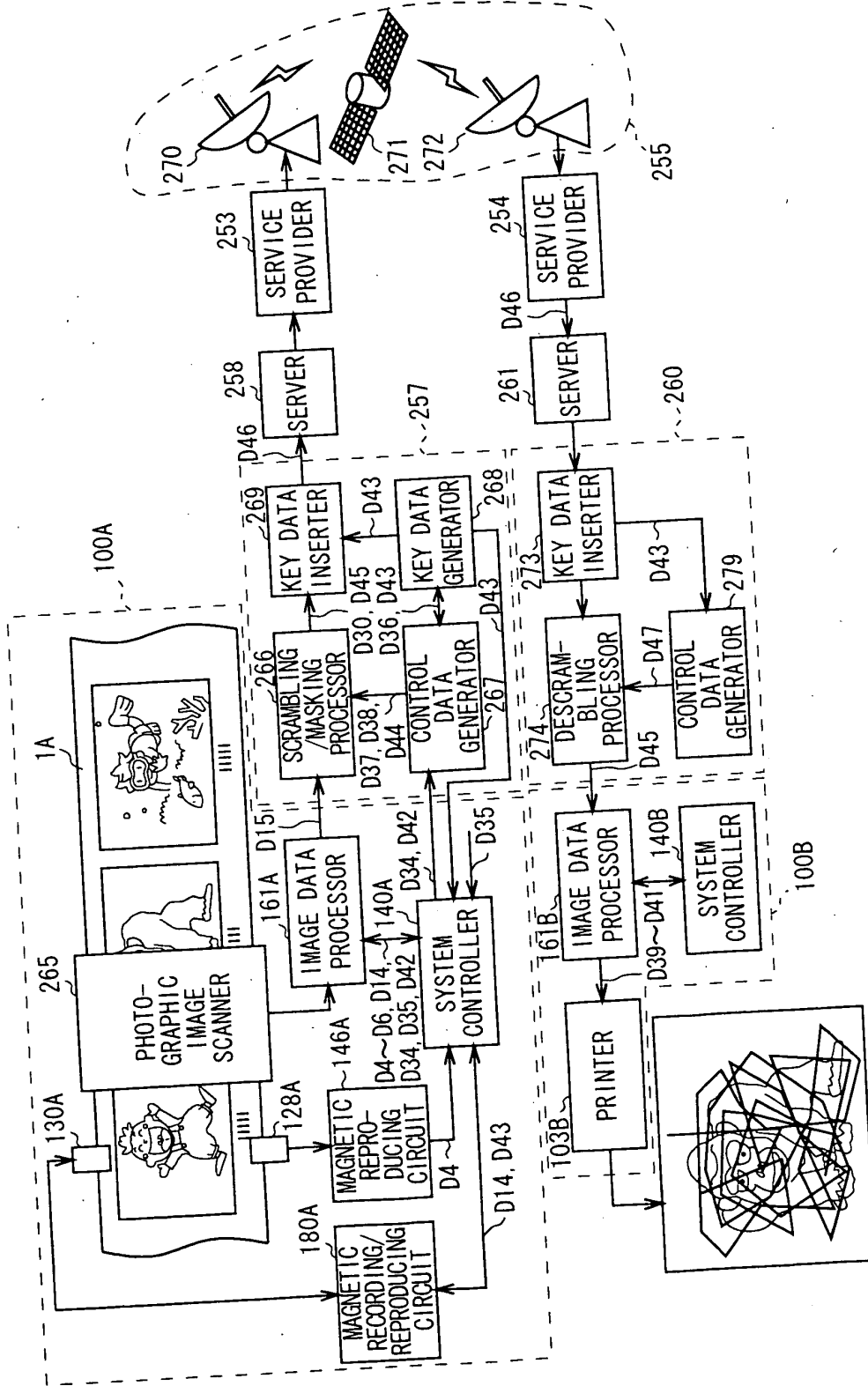


FIG.26

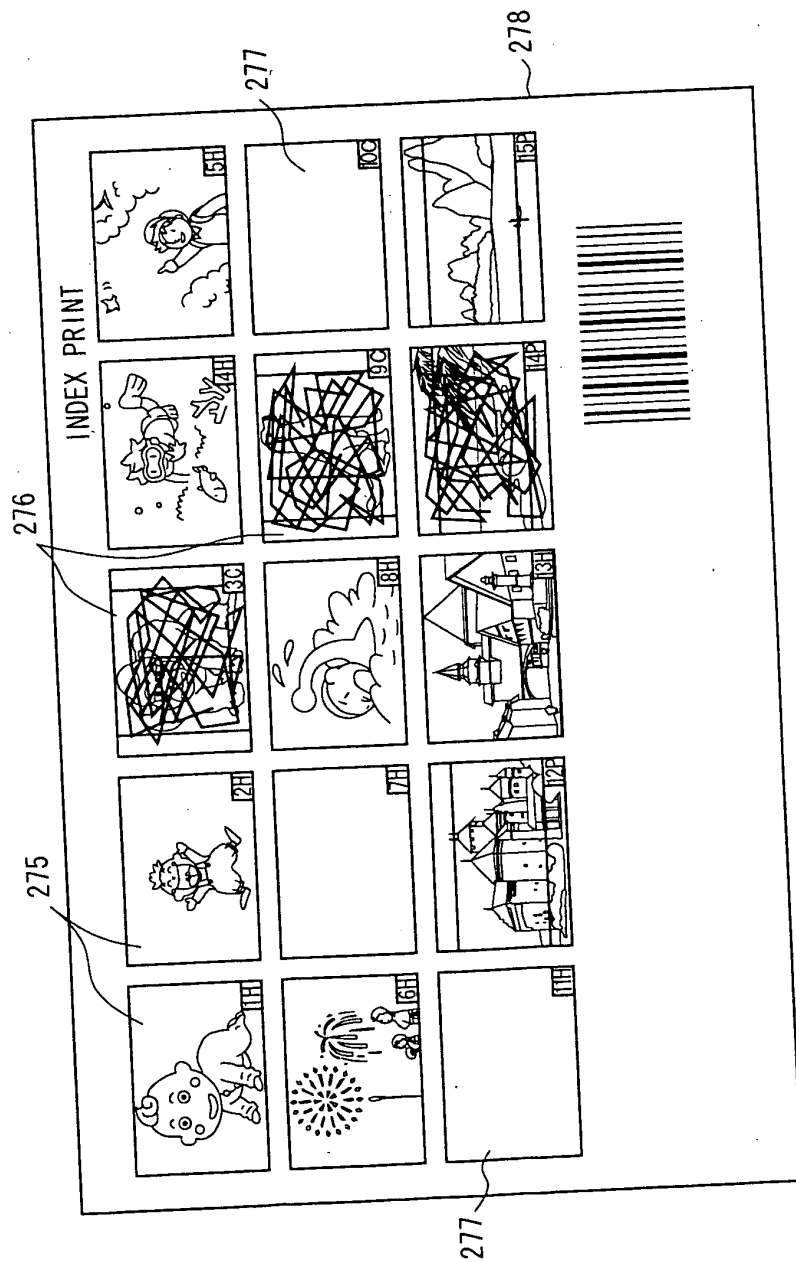


FIG. 27

FIG. 28 is a schematic diagram of a system for displaying a sequence of images. The system includes a display device 275 and a control unit 280. The display device 275 is configured to display a sequence of images 281, 282, 283, 284, 285, 286, 287, 288, 289, and 290. The control unit 280 is configured to control the display device 275 to display the sequence of images 281, 282, 283, 284, 285, 286, 287, 288, 289, and 290.

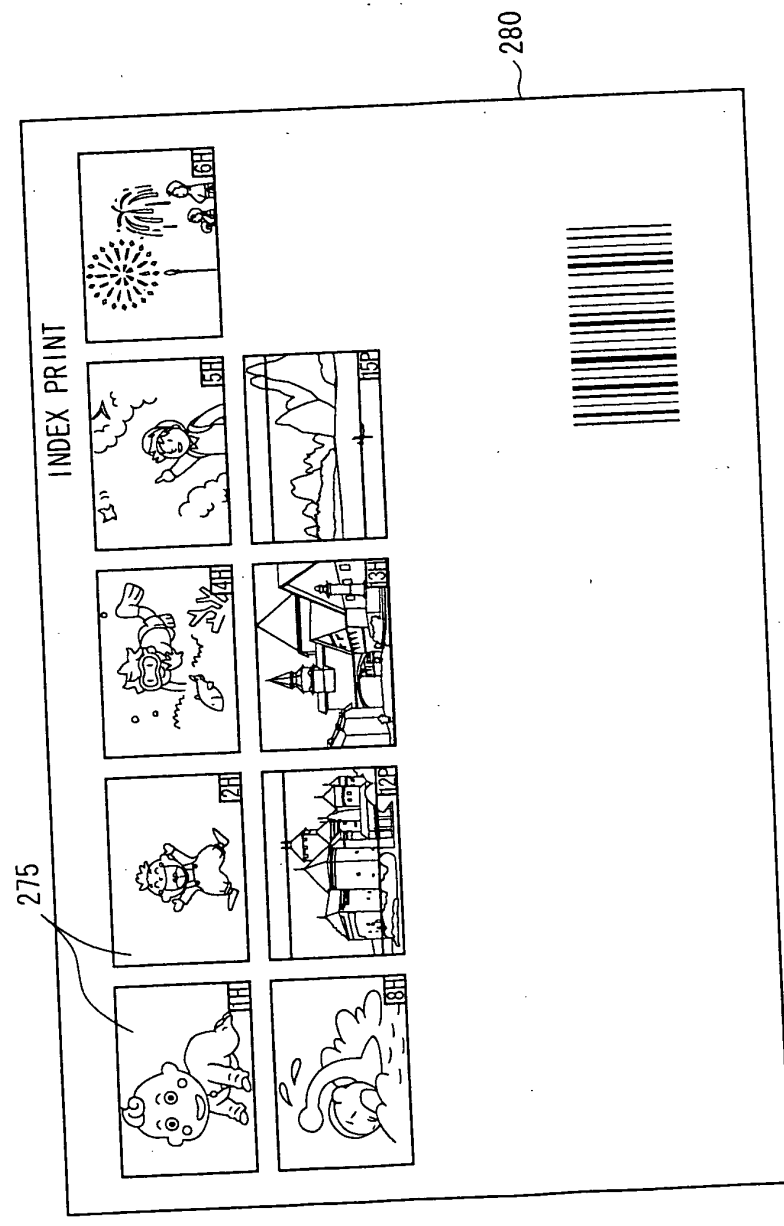


FIG.28

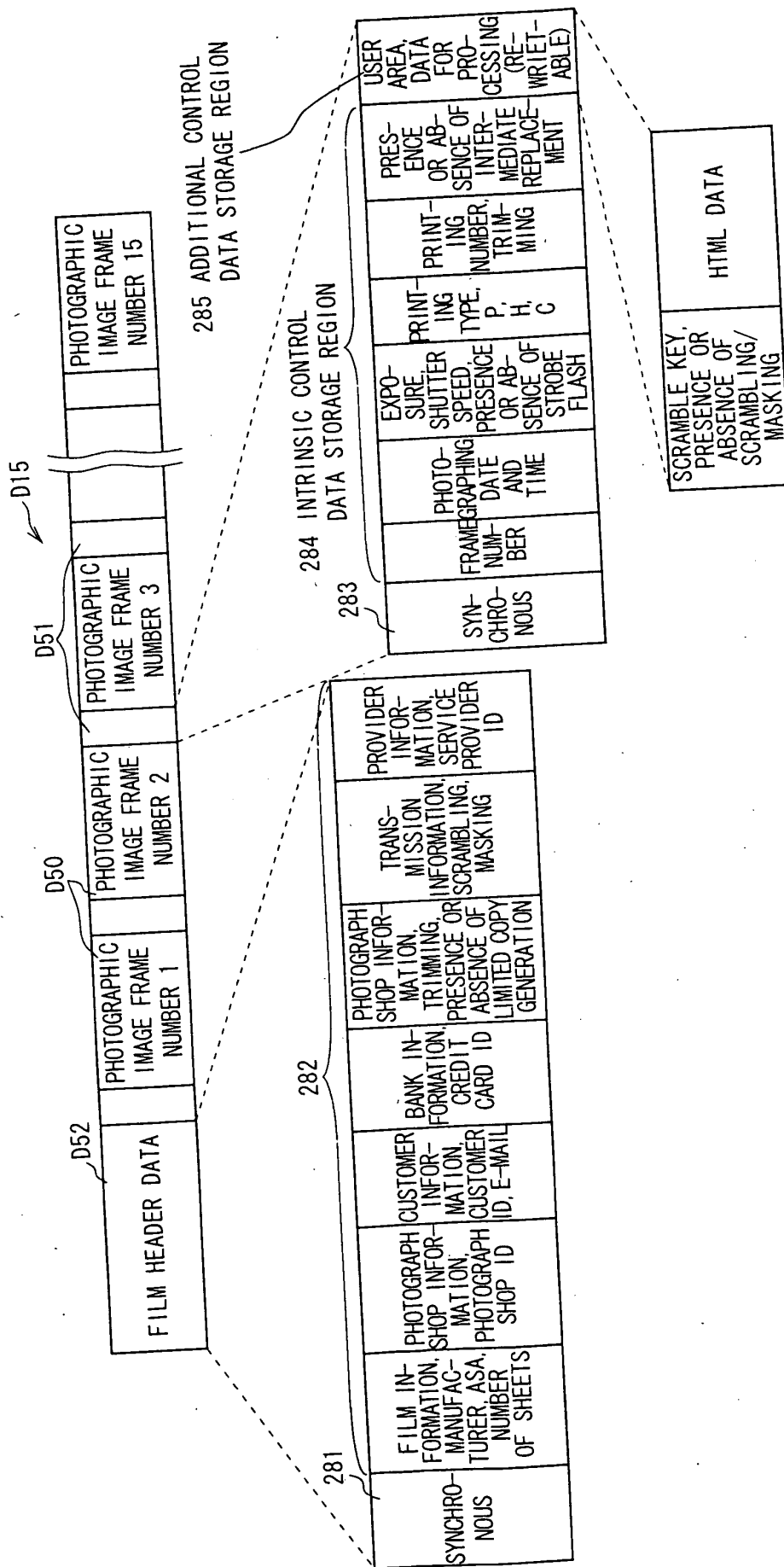


FIG. 29

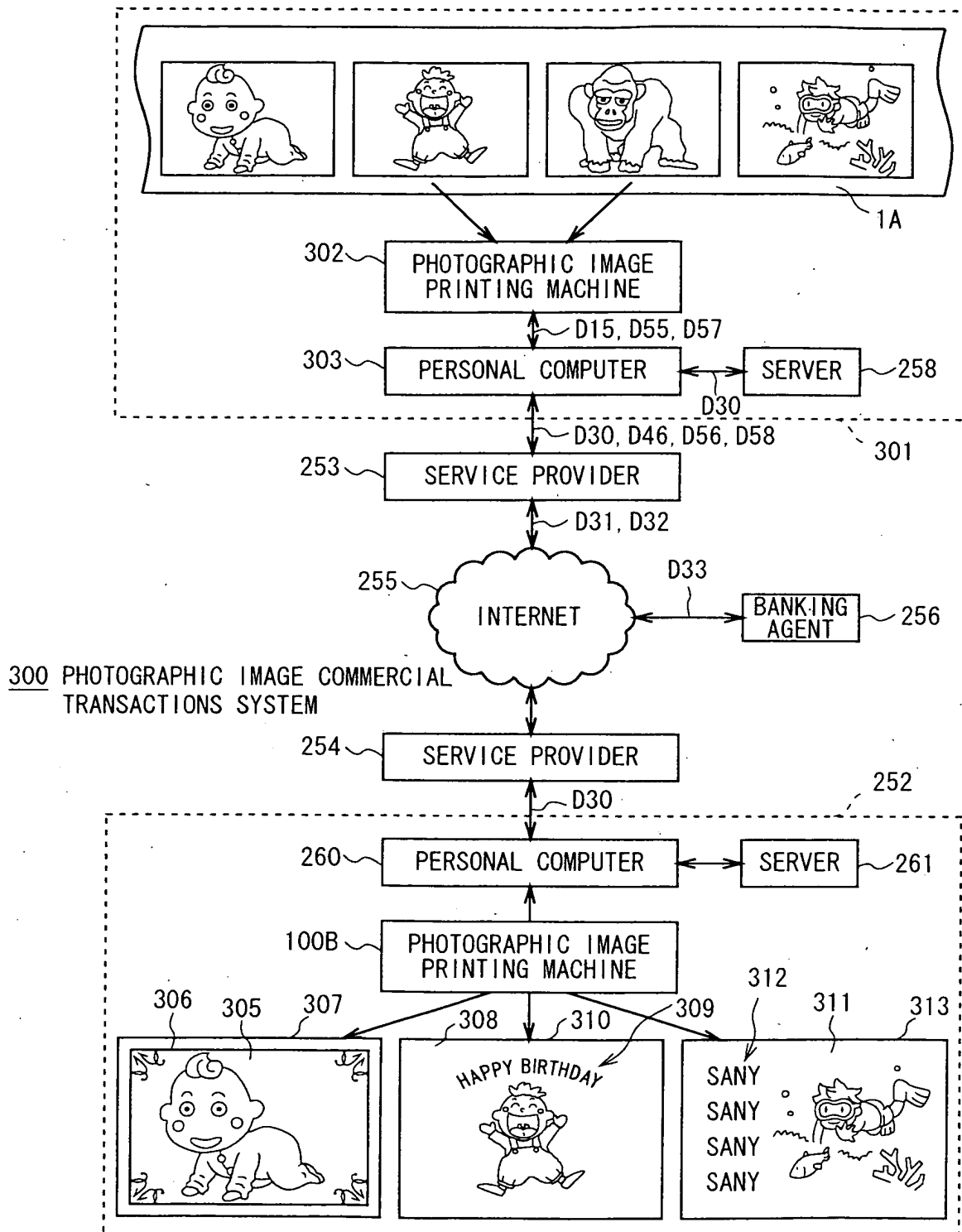


FIG.30



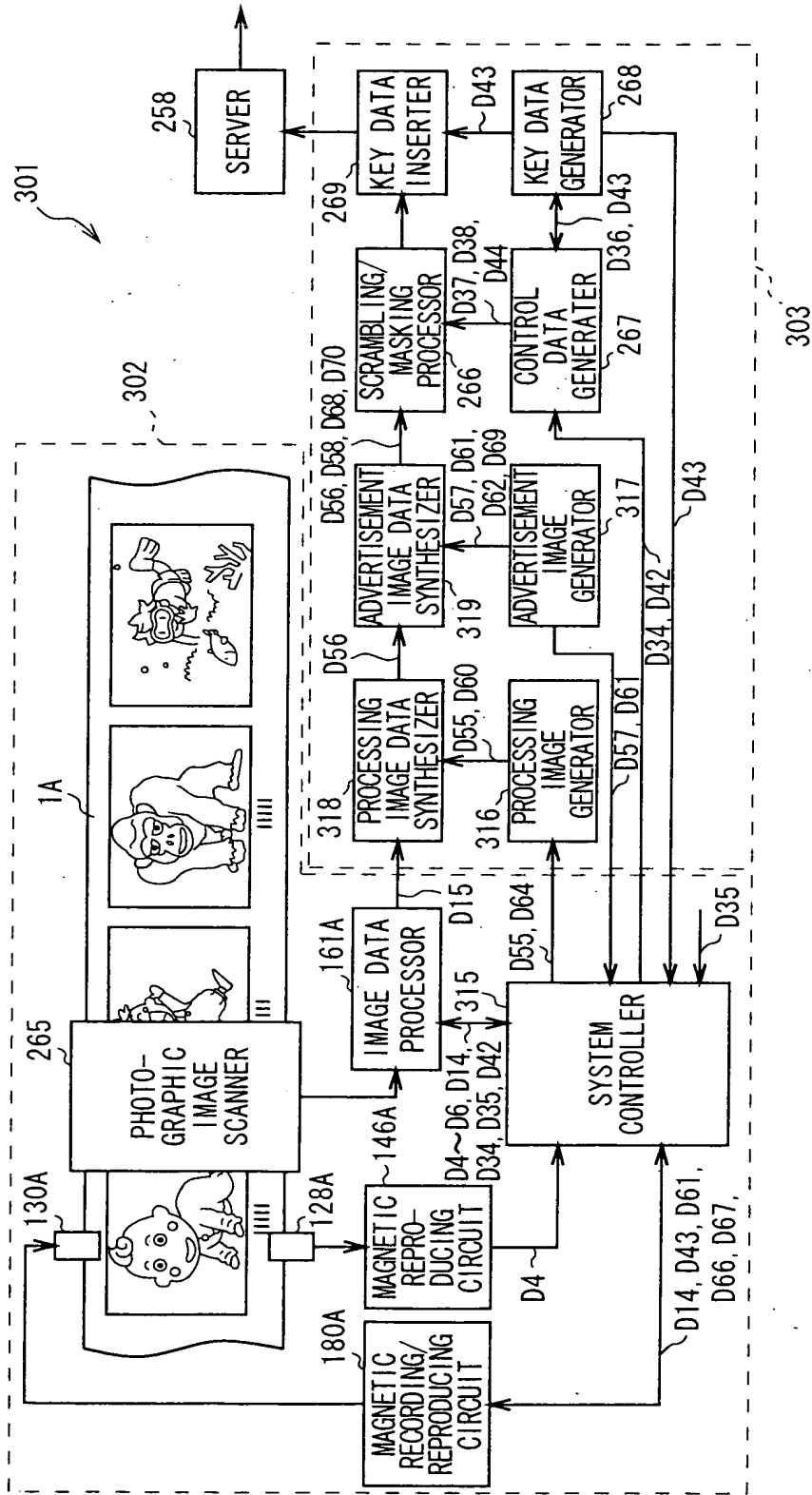


FIG.31

FIG.32A



FIG.32B



FIG.32C



FIG.33A

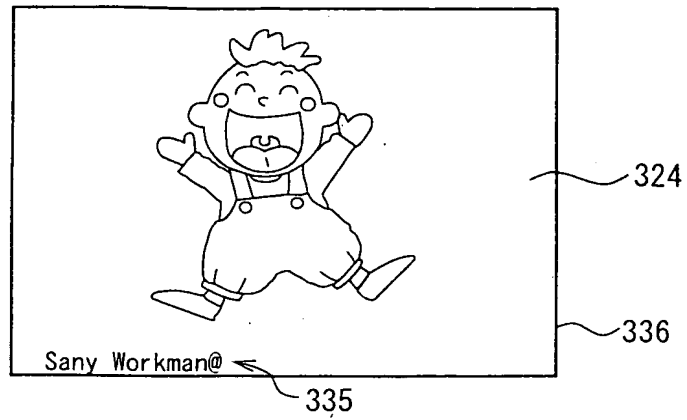


FIG.33B

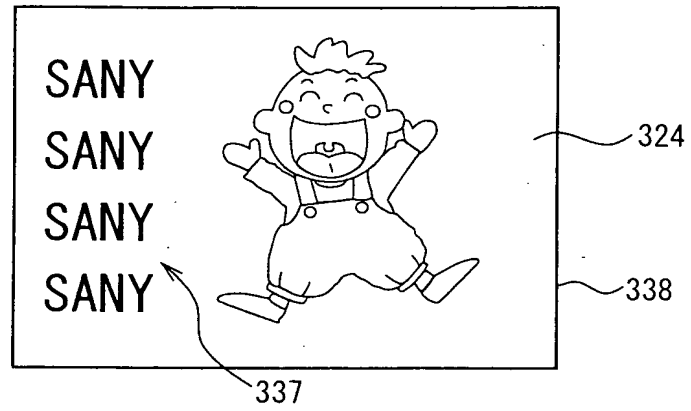
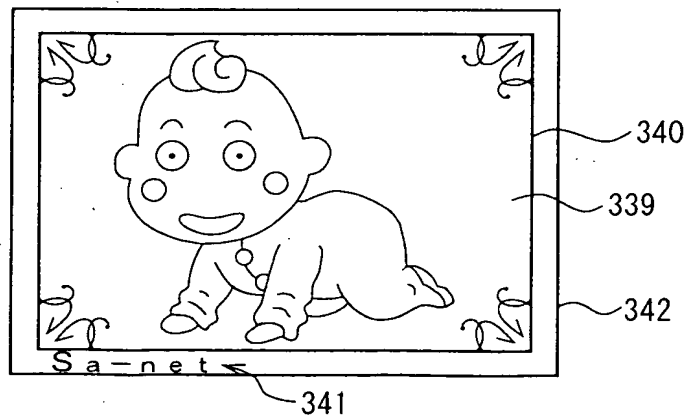


FIG.33C



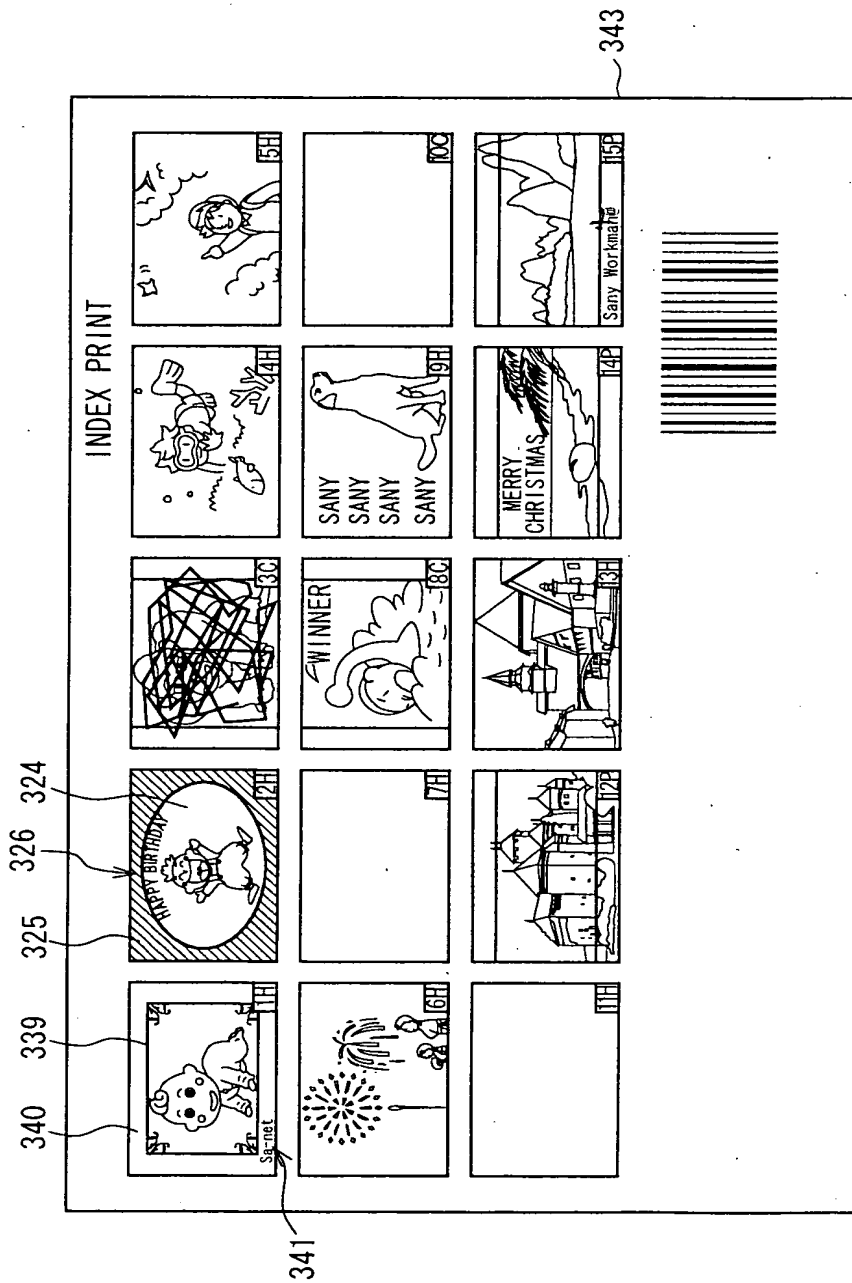


FIG. 34

FIG.35A

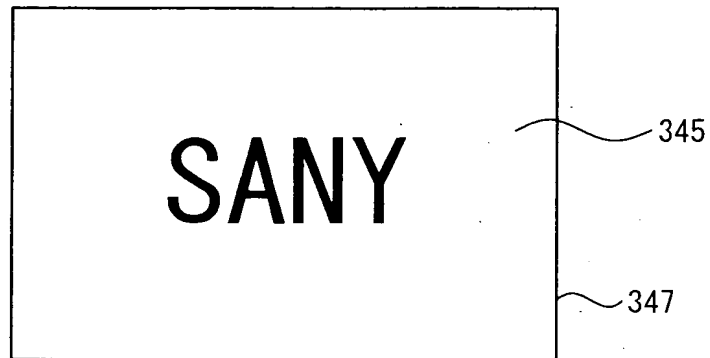


FIG.35B

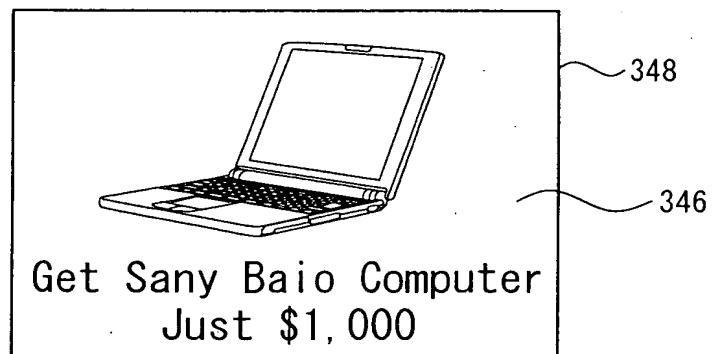




FIG. 36

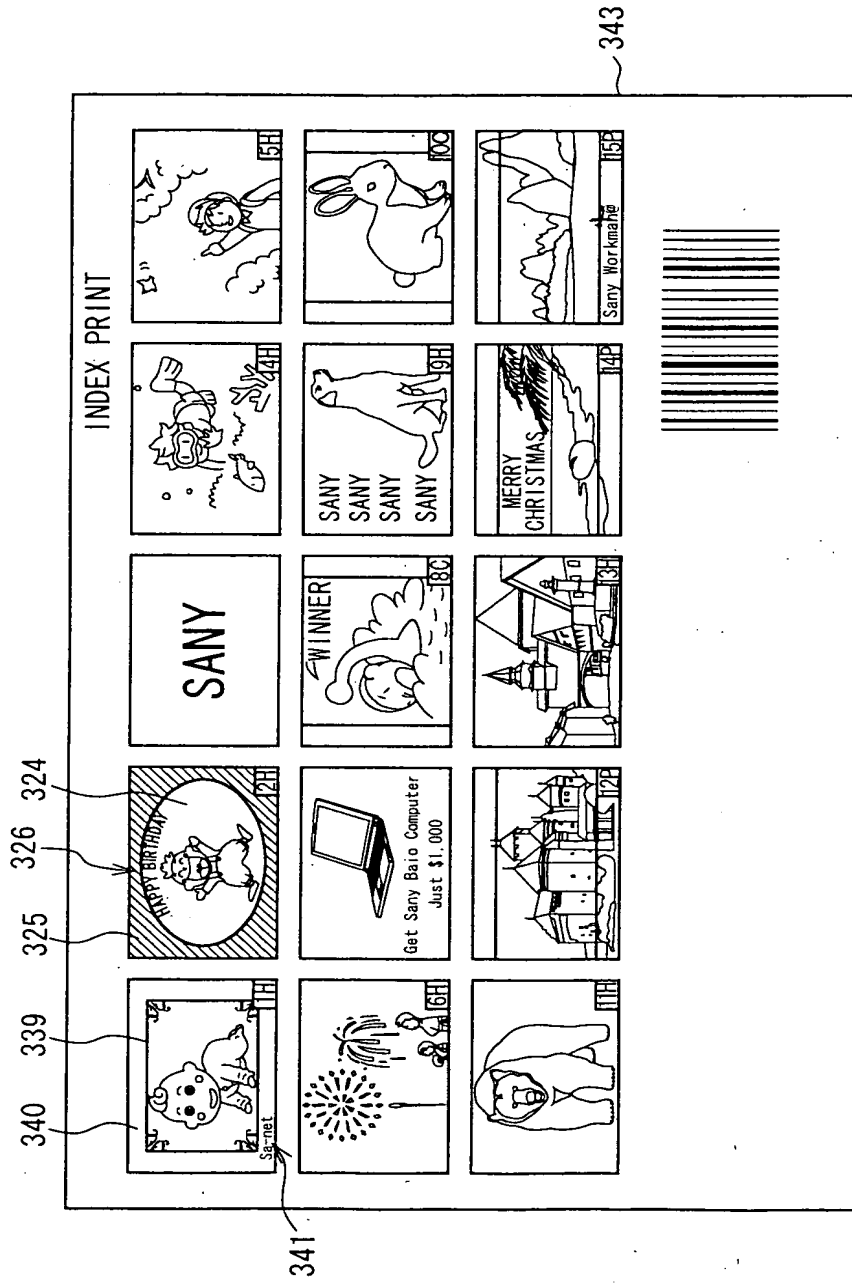


FIG.37





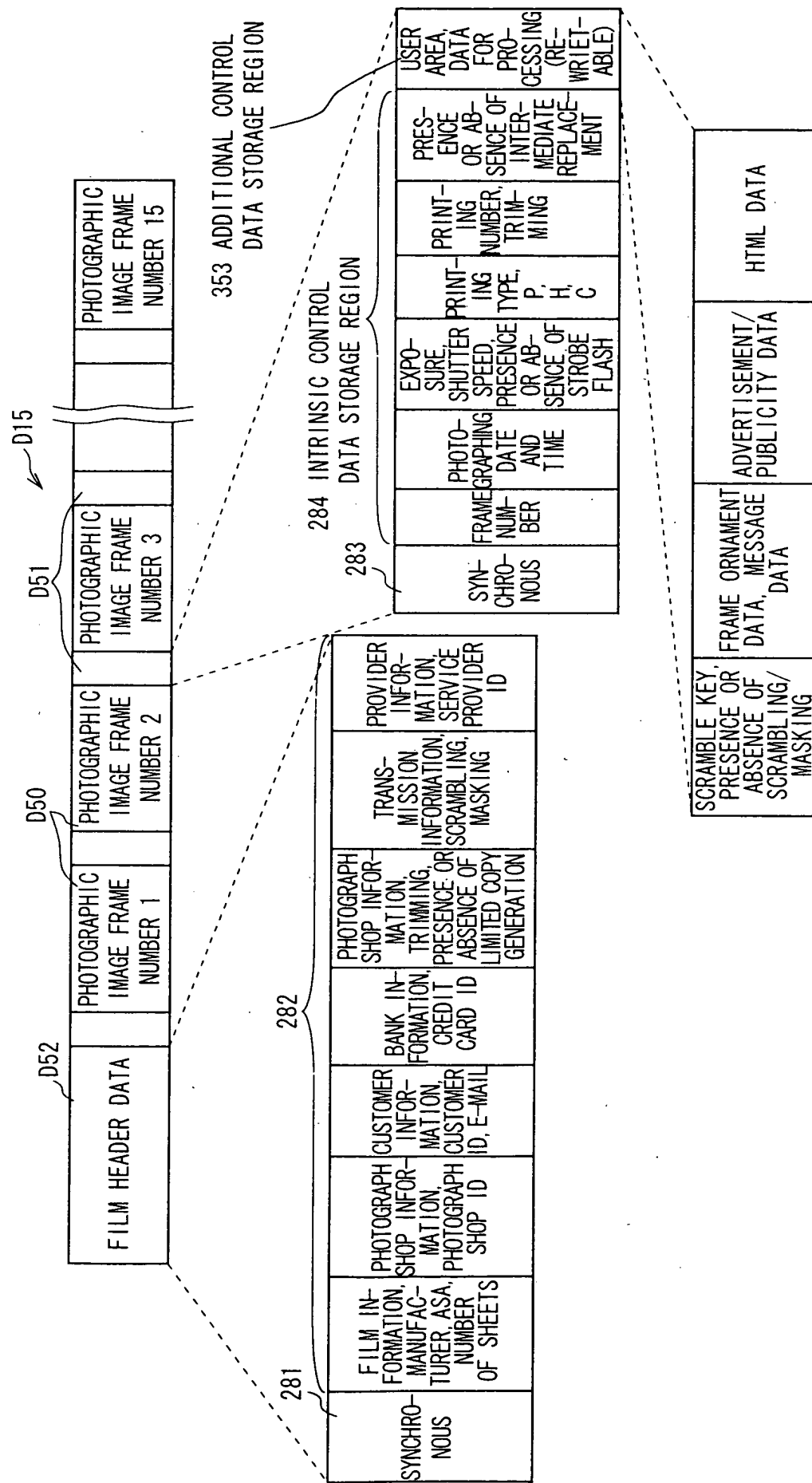


FIG.39

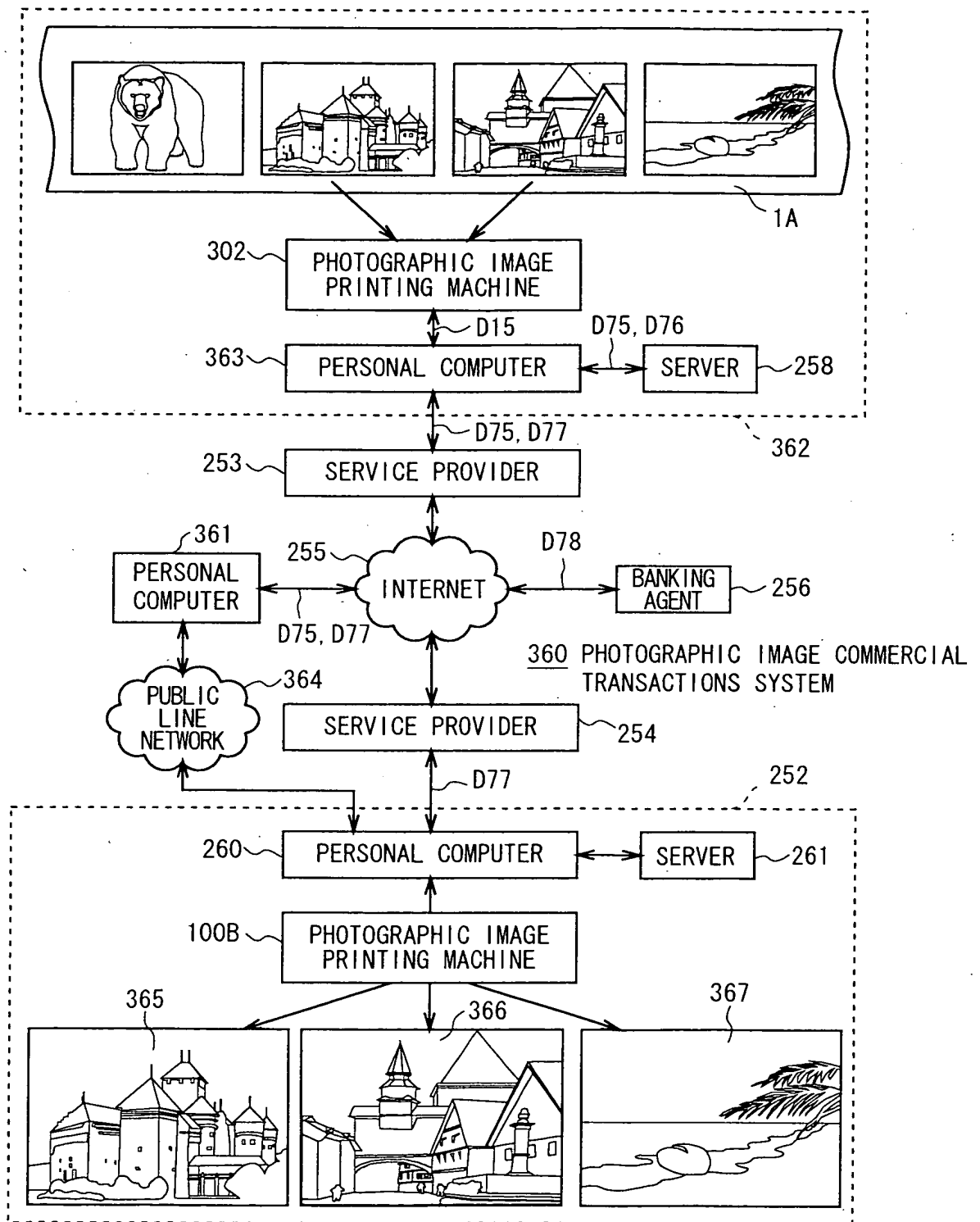


FIG.40

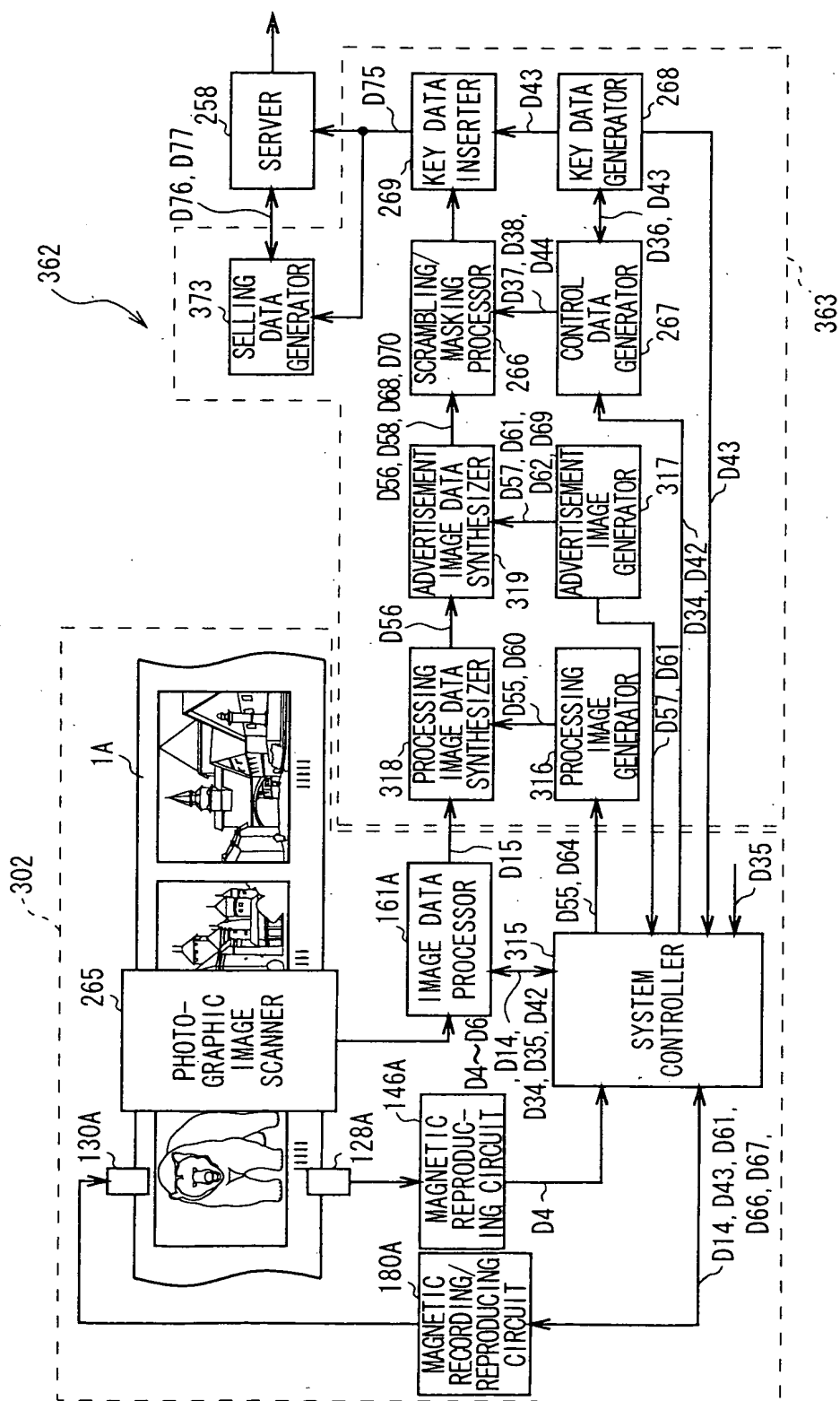


FIG. 41

FIG. 42 is a block diagram of a data structure for a photographic film strip. The diagram shows a sequence of data blocks: a FILM HEADER DATA block, followed by a series of PHOTOGRAPHIC IMAGE FRAME blocks (NUMBER 1 to NUMBER 15), and finally a PHOTOGRAPHIC IMAGE FRAME NUMBER 15 block. The FILM HEADER DATA block is divided into three sections: D80, D50, and D81. D80 contains PHOTOGRAPHIC IMAGE FRAME NUMBER 1, PHOTOGRAPHIC IMAGE FRAME NUMBER 2, and PHOTOGRAPHIC IMAGE FRAME NUMBER 3. D50 contains PHOTOGRAPHIC IMAGE FRAME NUMBER 1. D81 contains PHOTOGRAPHIC IMAGE FRAME NUMBER 2 and PHOTOGRAPHIC IMAGE FRAME NUMBER 3. The PHOTOGRAPHIC IMAGE FRAME blocks are divided into two sections: 281 and 283. 281 contains SYNCHRONOUS, FILM INFORMATION, MANUFACTURER, ASA, NUMBER OF SHEETS, PHOTOGRAPHIC SHOP INFORMATION, PHOTOGRAPHIC SHOP ID, CUSTOMER INFORMATION, CUSTOMER ID, E-MAIL, BANK INFORMATION, CREDIT CARD ID, PHOTOGRAPHIC SHOP INFORMATION, TRIMMING, PRESENCE OF LIMITED COPY GENERATION, TRANSMISSION INFORMATION, SCRAMBLING, MASKING, PROVIDER INFORMATION, SERVICE, PROVIDER ID. 283 contains SYNCHRONOUS, PHOTOGRAPHIC DATE AND TIME, EXPOSURE SHUTTER SPEED, OR ABSENCE OF STROBE FLASH, PRINTING TYPE, P, H, C, PRINTING NUMBER, TRIMMING, PRESENCE OF INTERMEDIATE REPLACEMENT, PRESENCE OF ABSENCE OF INTERMEDIATE REPLACEMENT, USER AREA, DATA FOR PROCESSING (RE-WRITEABLE).

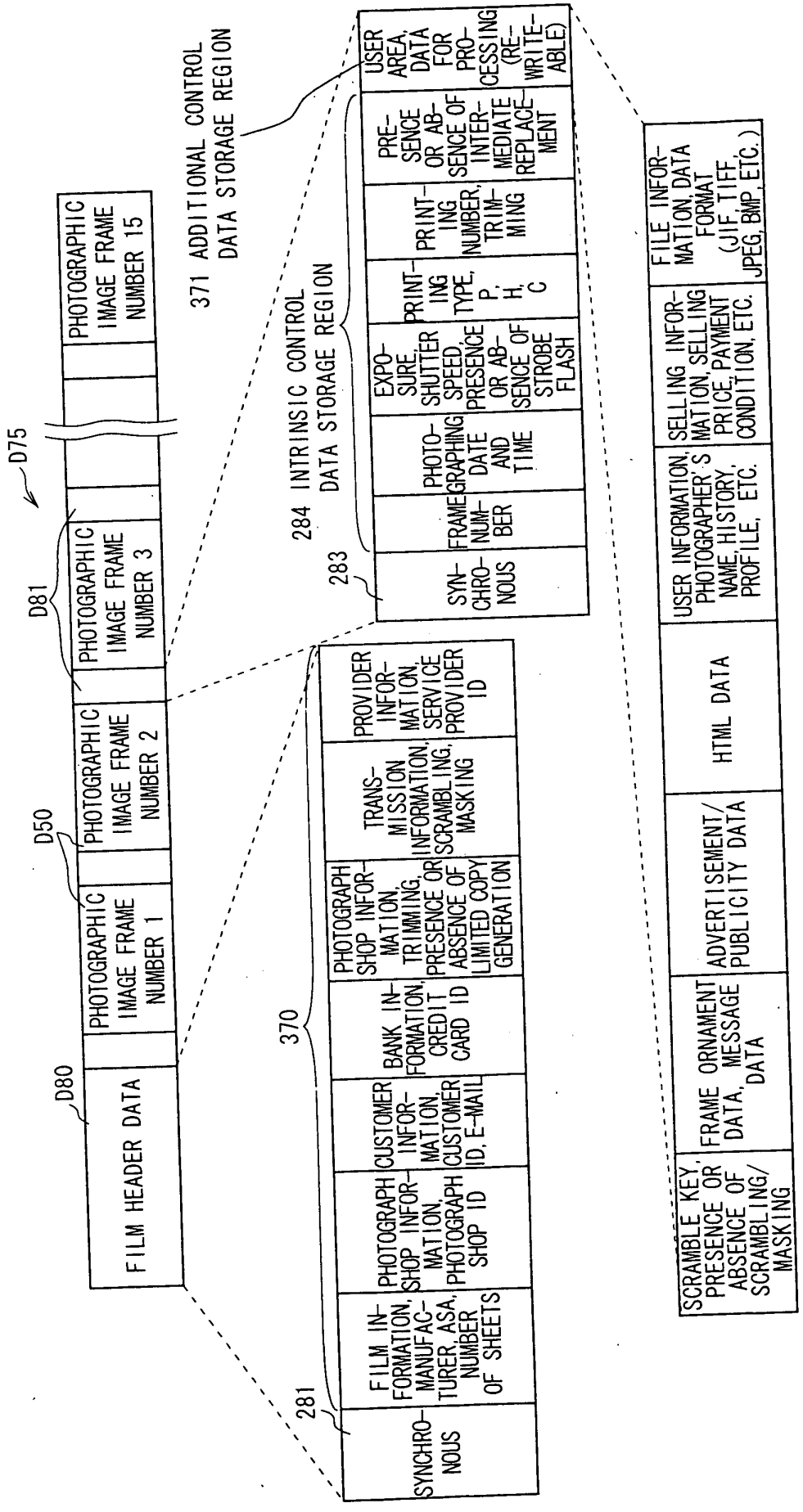


FIG.42

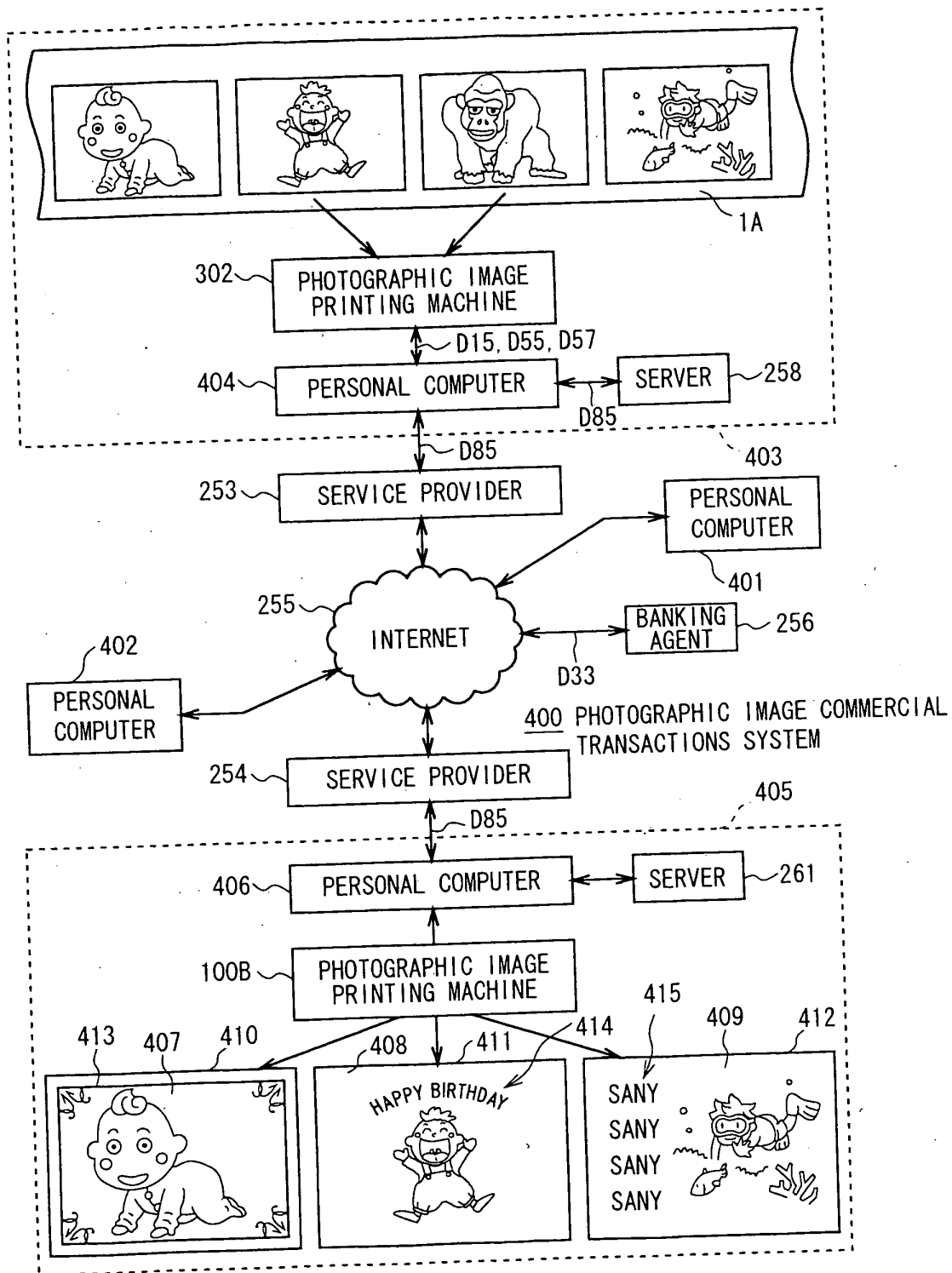


FIG.43

FIG. 44 is a perspective view of the forward content display screen 417 of the device 100, showing the screen 417 with the grid of 20 rectangular display areas 418 arranged in 4 rows and 5 columns. The screen 417 is mounted on a bezel 416. A small square button 419 is located on the left side of the bezel 416. The screen 417 is shown in a perspective view, indicating its depth and orientation.

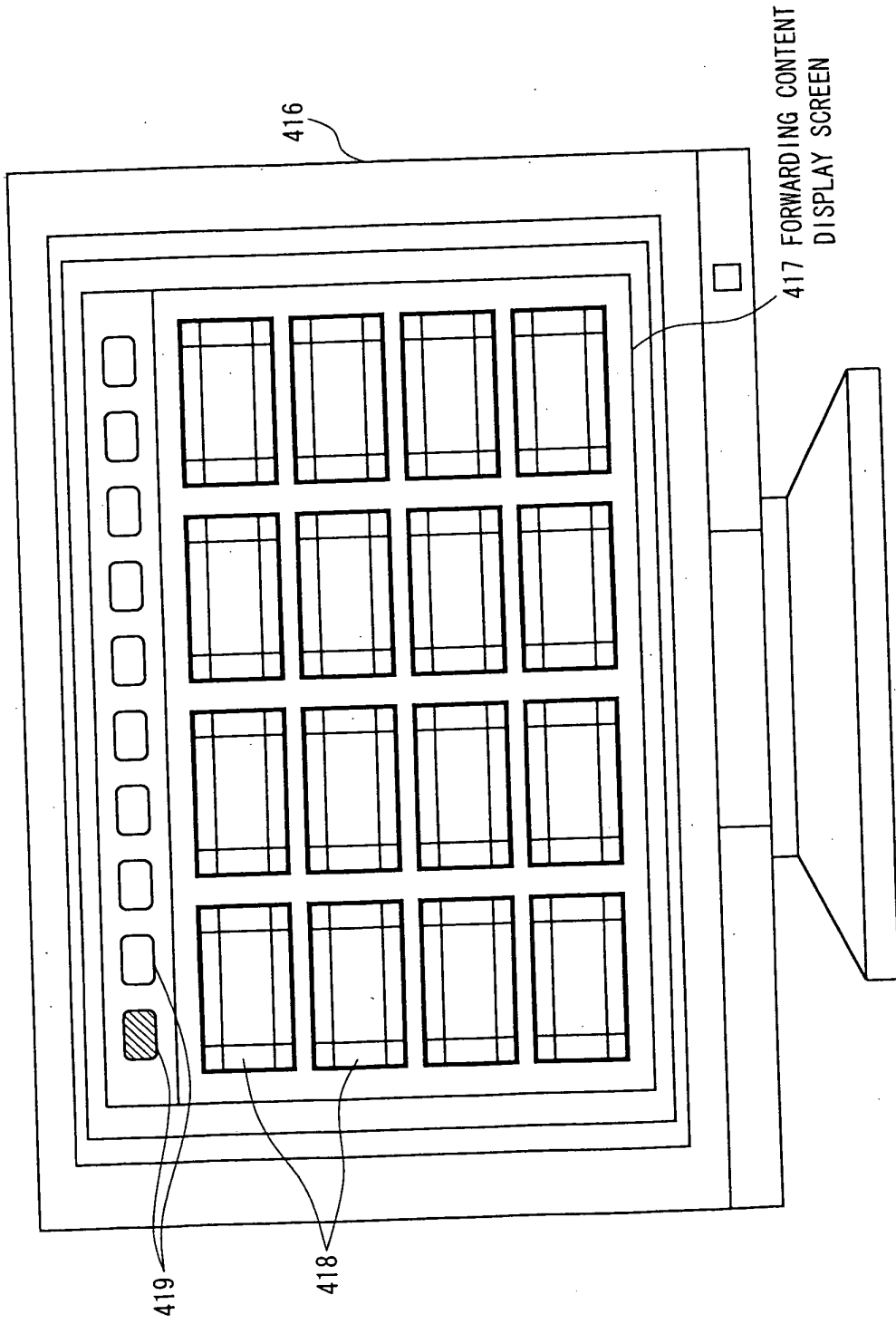


FIG. 44

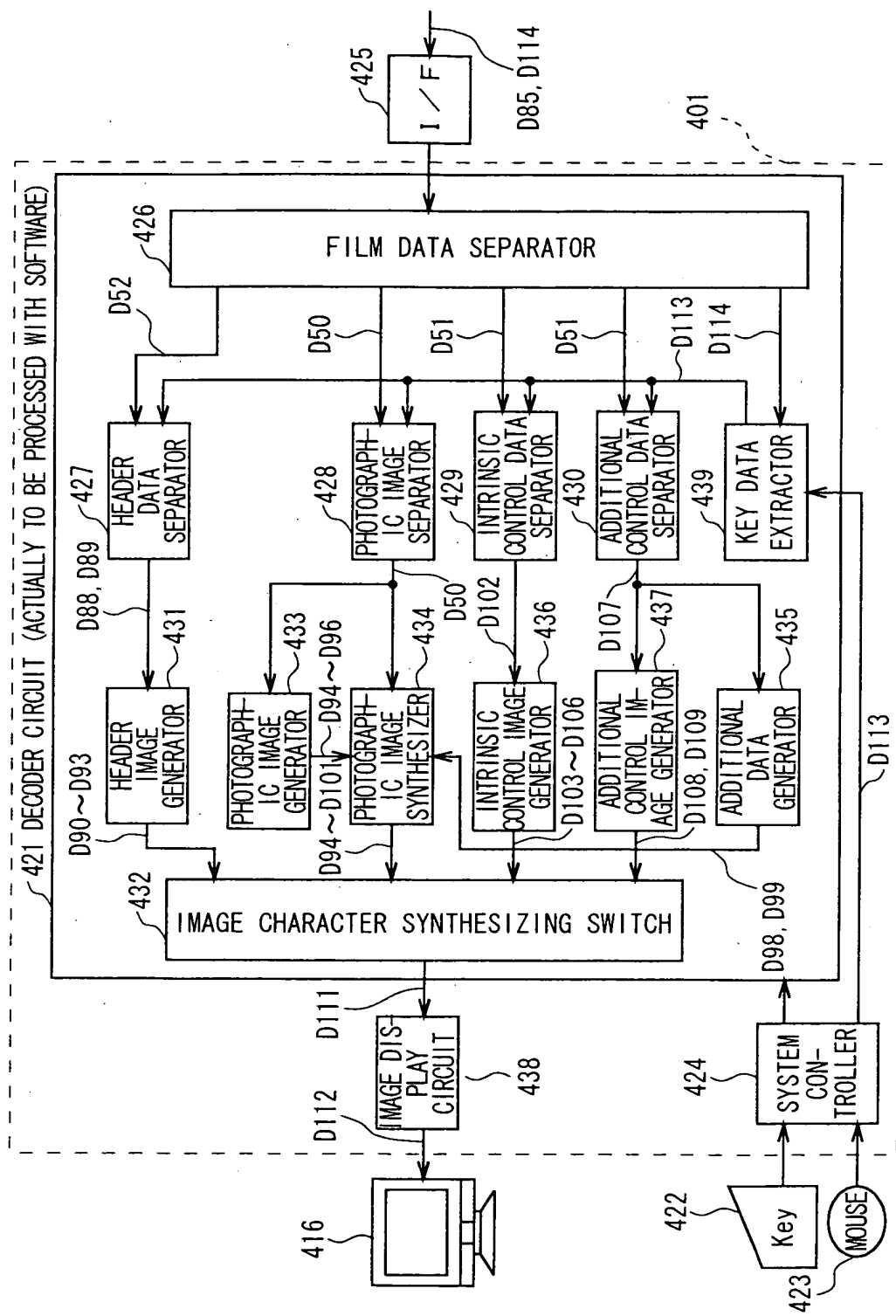


FIG.45

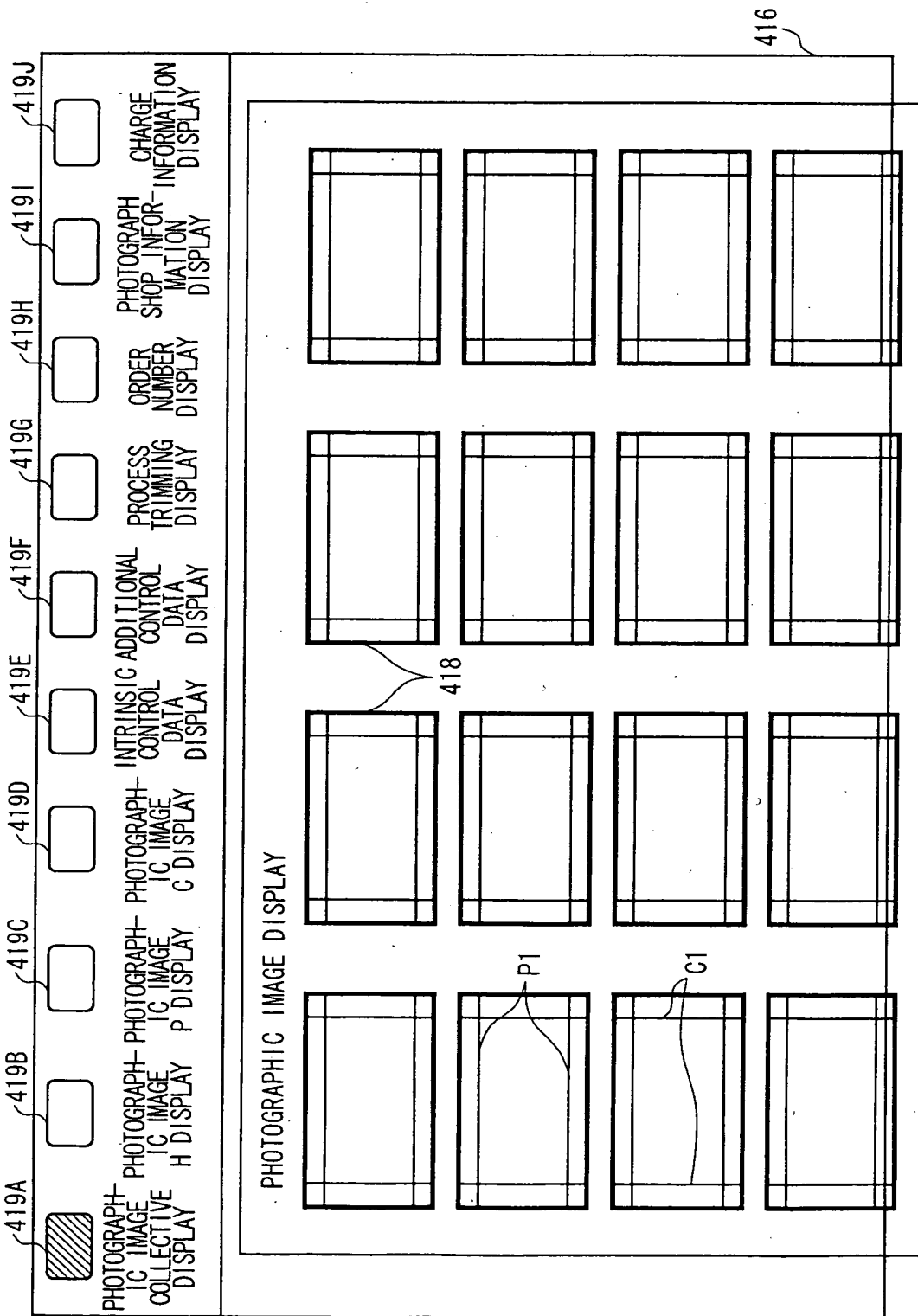


FIG.46



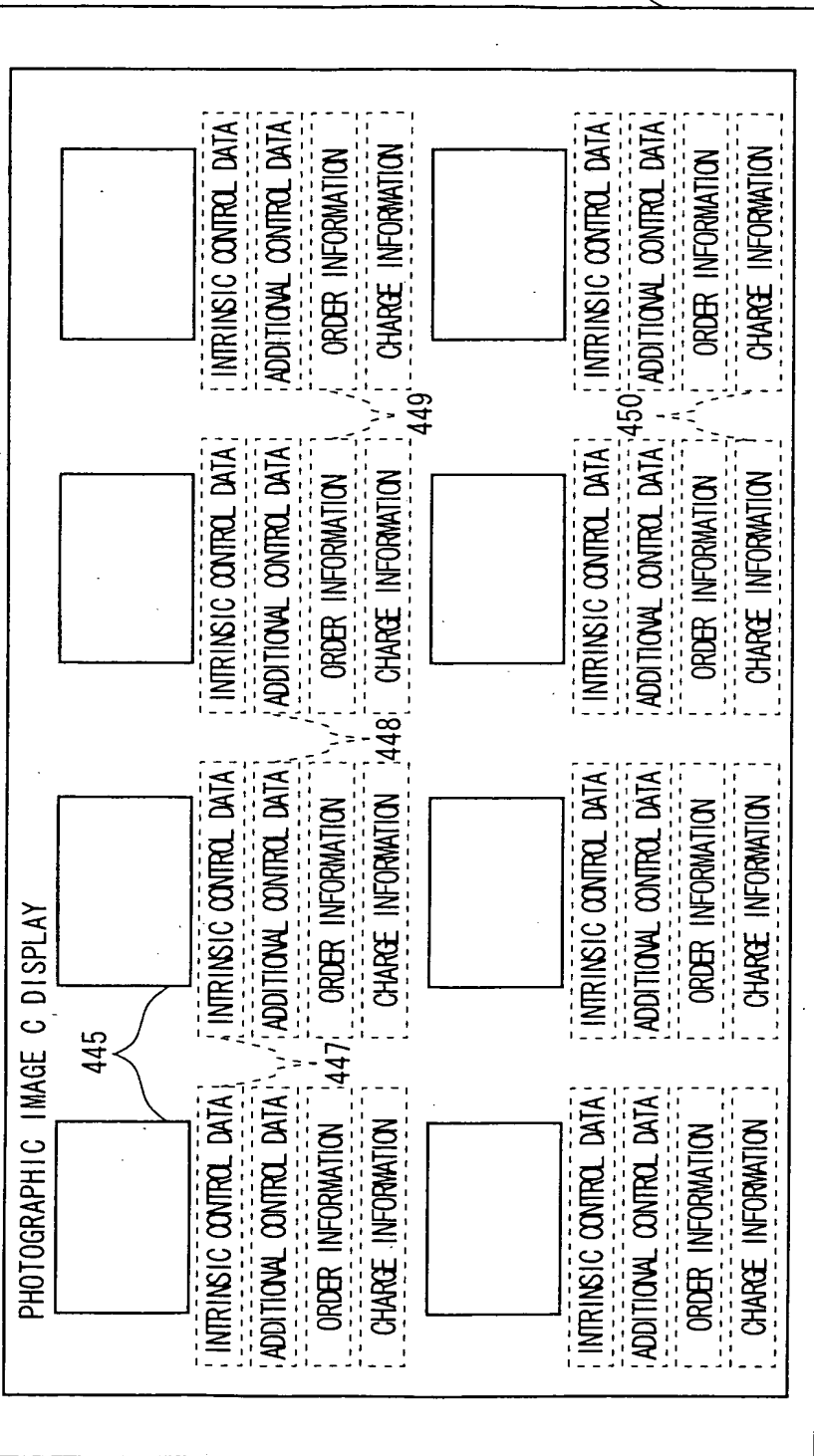
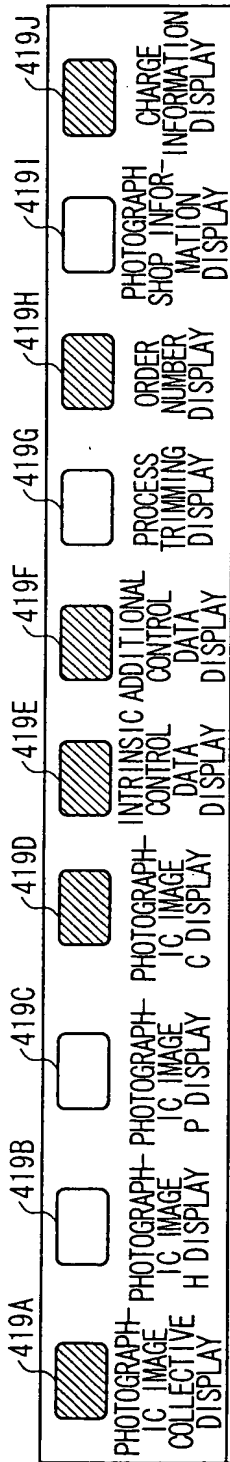


FIG.47

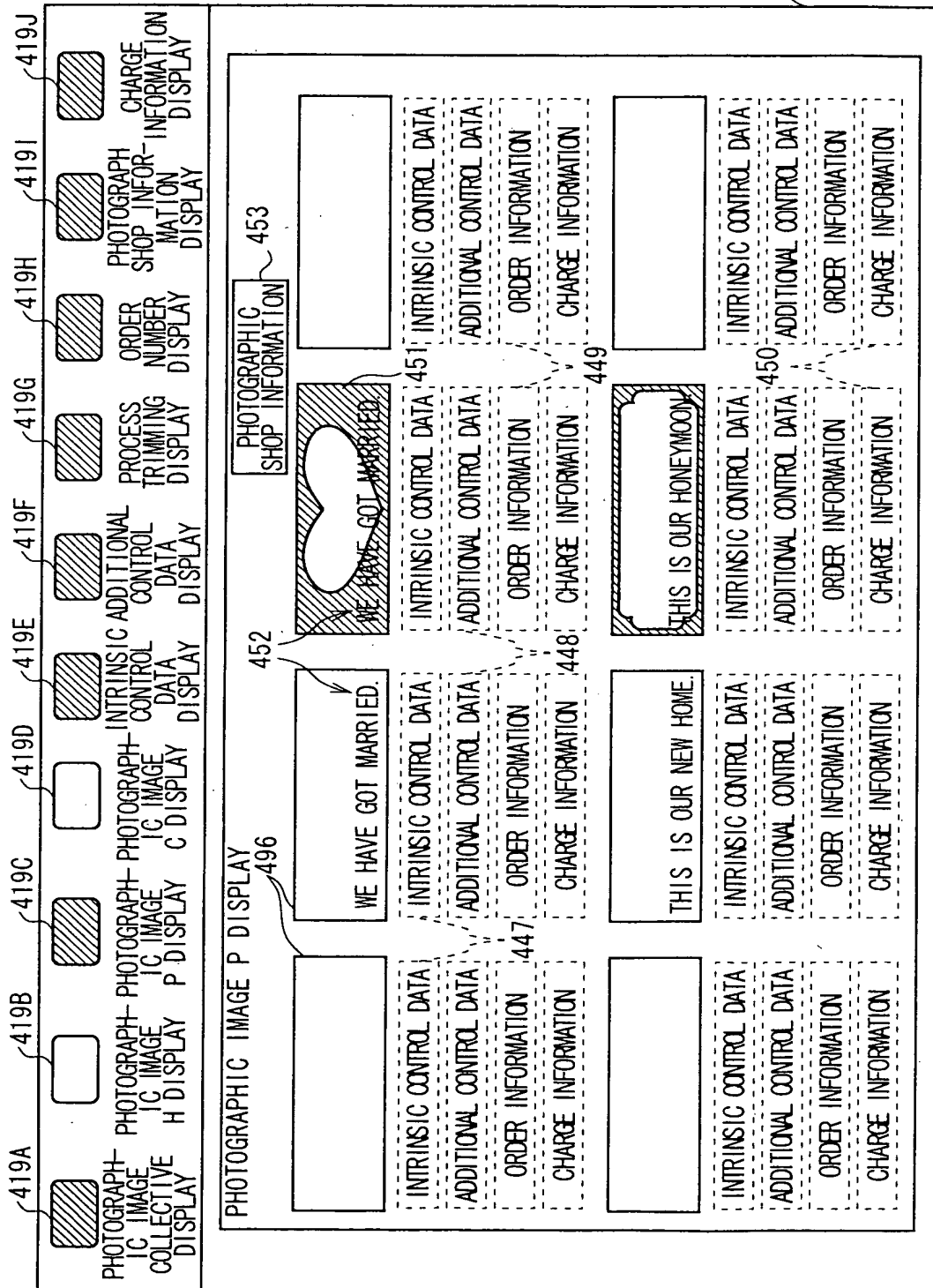


FIG. 48

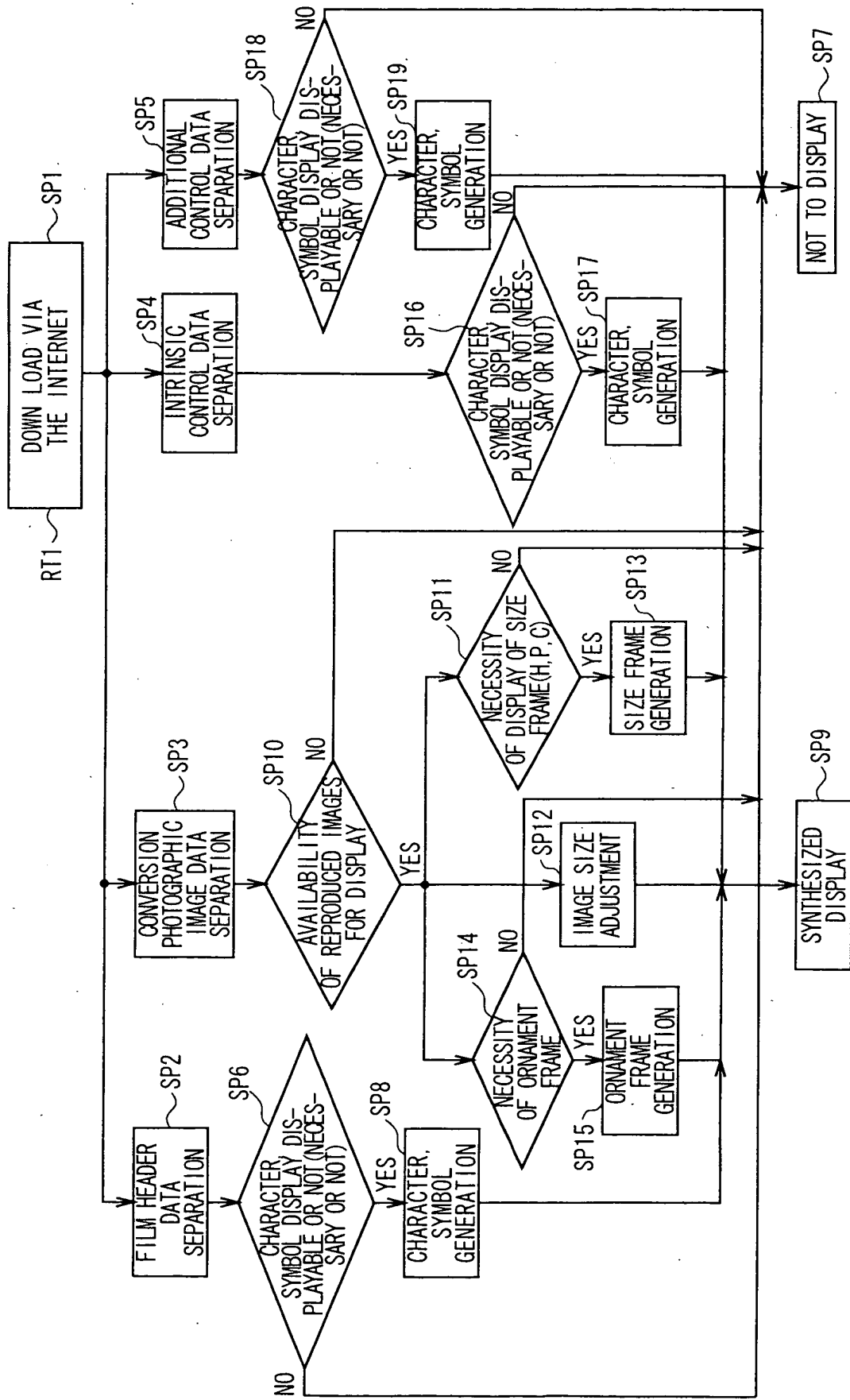


FIG.49

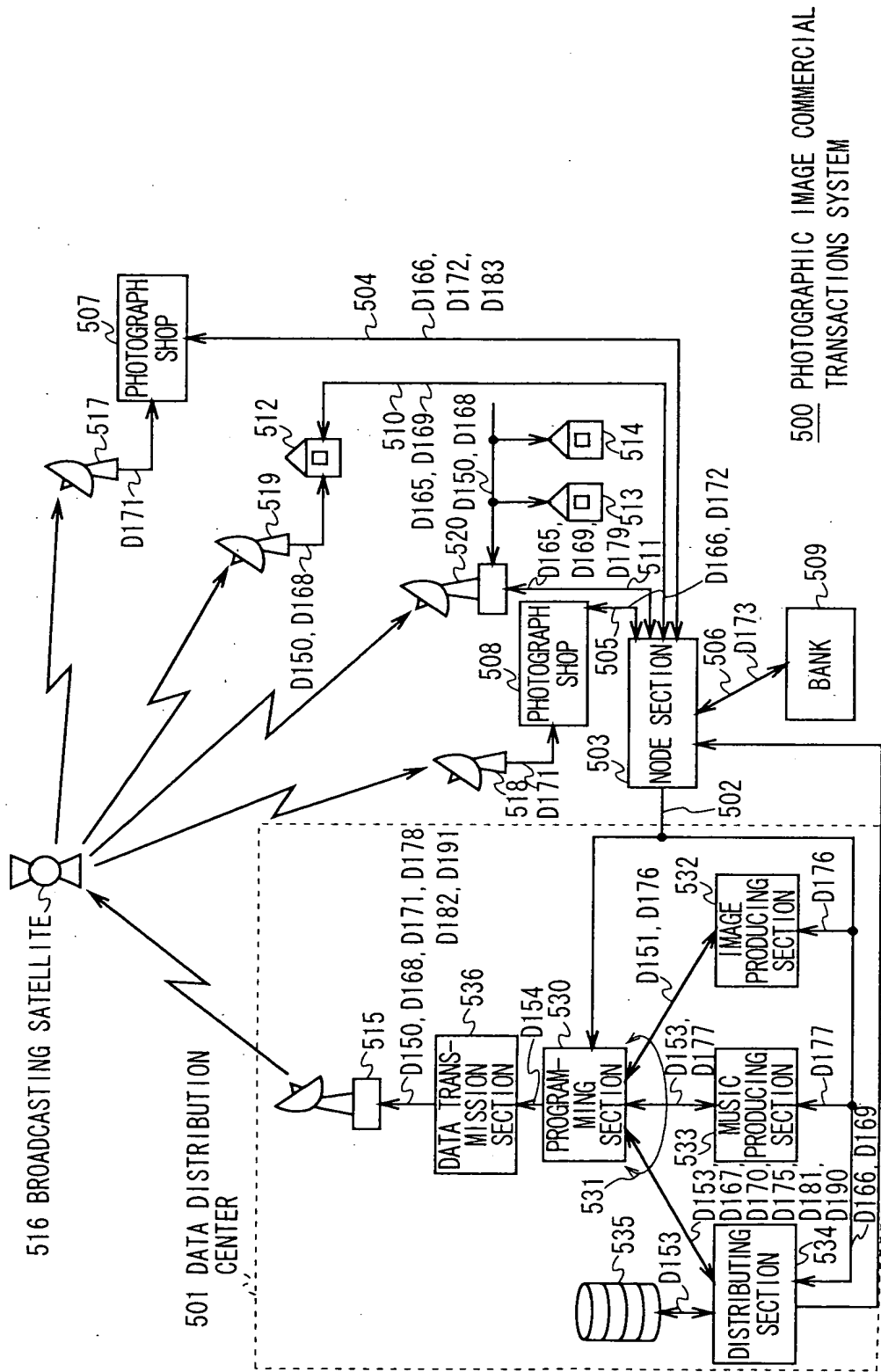


FIG.50

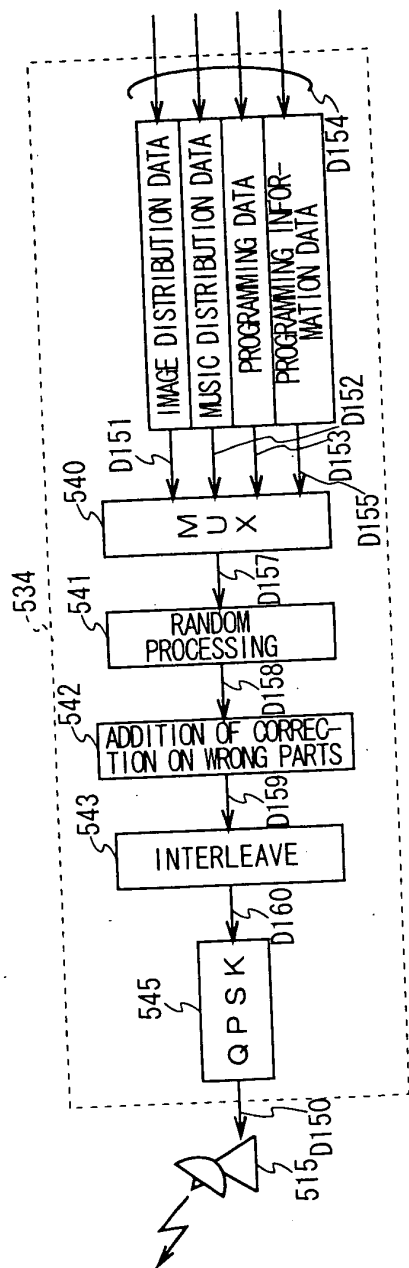


FIG. 51

FIG. 52 is a block diagram of a satellite broadcast receiver system. The system includes a satellite broadcast receiver (550) and a home server (565). The receiver (550) is connected to the home server (565) via a network (522A). The receiver (550) includes a QPSK modulator (555), a deinterleaver (556), a correction on wrong parts (557), a random processing (558), a demux (559), and a monitor (565). The receiver (550) also includes a data input (519, 520) and a data output (D150, D168). The home server (565) includes a monitor (565) and a data output (D155, D151~D153, D176). The receiver (550) is connected to the home server (565) via a network (522A). The receiver (550) also includes a data input (519, 520) and a data output (D150, D168). The home server (565) includes a monitor (565) and a data output (D155, D151~D153, D176).

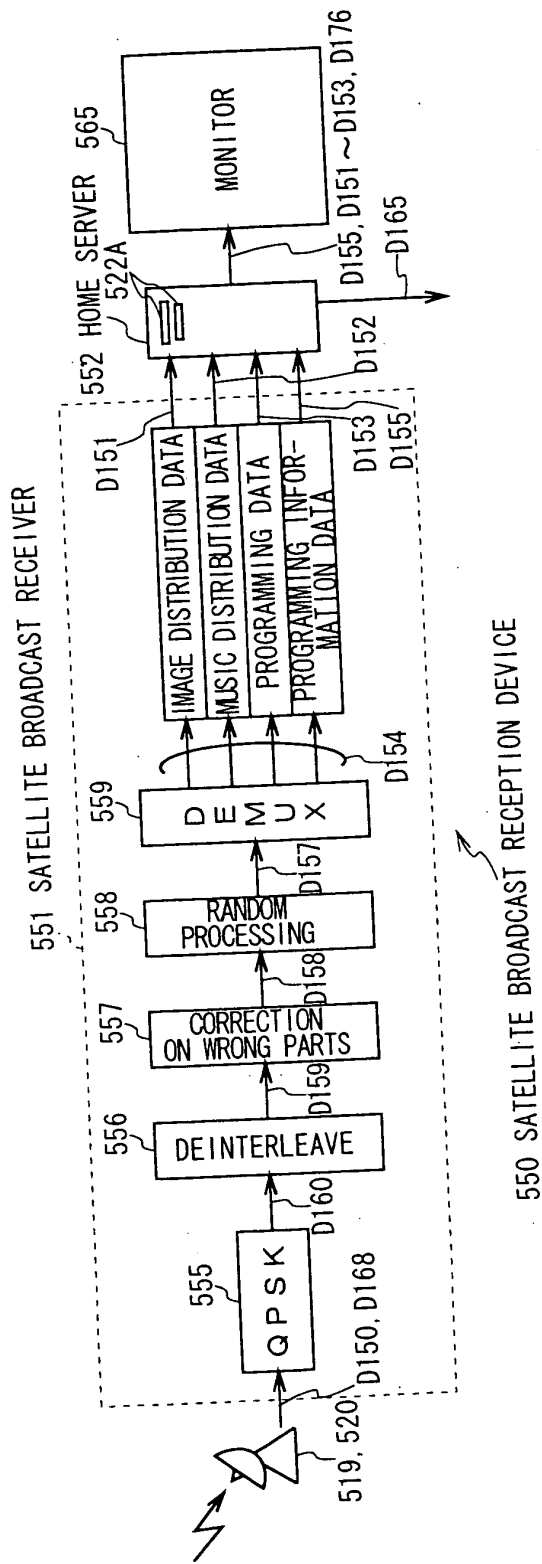


FIG.52

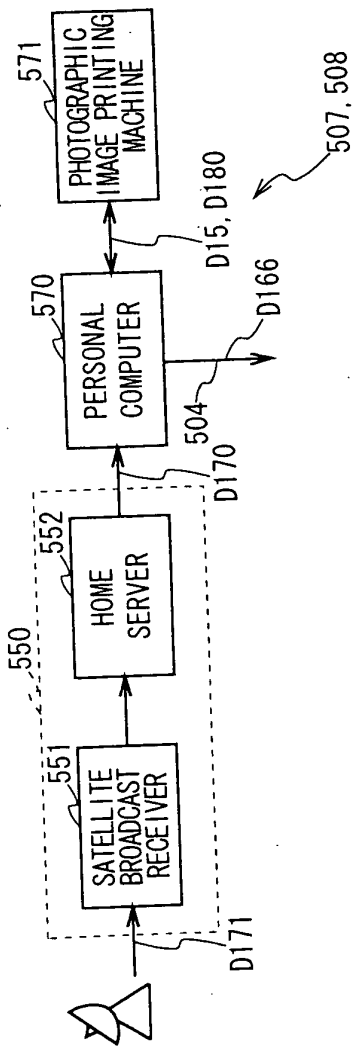





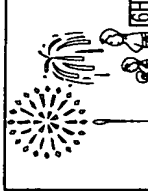



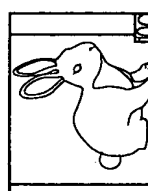


FIG.53

FIG. 54 is a schematic diagram of a system for processing orders for personalized items. The system includes a user interface 574, a database 575, a processing unit 576, and a confirmation screen 578. The user interface 574 displays a grid of order items, each with a unique identifier (e.g., 11H, 12H, 13H, 14H, 15H, 16H, 17H, 18H, 19H, 20H) and a corresponding image of the item. The database 575 stores information about the items and the user's order. The processing unit 576 manages the order processing flow. The confirmation screen 578 displays the confirmed order details, including the item identifier, the item image, and the user's confirmation status (CONFIRMED).

574

ORDER CONFIRMATION SCREEN				
 11H	 12H HAPPY BIRTHDAY	 13H	 14H	 15H
PRINTING TYPE	PRINTING TYPE	PRINTING TYPE	PRINTING TYPE	PRINTING TYPE
PRINTING NUMBER	PRINTING NUMBER	PRINTING NUMBER	PRINTING NUMBER	PRINTING NUMBER
PRINTING SIZE	PRINTING SIZE	PRINTING SIZE	PRINTING SIZE	PRINTING SIZE
PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING
PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER
 16H	 17H	 18H	 19H	 20H
PRINTING TYPE	PRINTING TYPE	PRINTING TYPE	PRINTING TYPE	PRINTING TYPE
PRINTING NUMBER	PRINTING NUMBER	PRINTING NUMBER	PRINTING NUMBER	PRINTING NUMBER
PRINTING SIZE	PRINTING SIZE	PRINTING SIZE	PRINTING SIZE	PRINTING SIZE
PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING	PRESENCE OR ABSENCE OF PROCESSING
PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER	PRESENCE OR ABSENCE OF ORDER

577

CONFIRMED ☒ 578

FIG.54



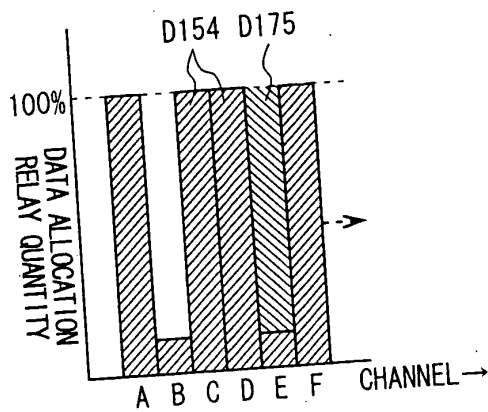


FIG.55

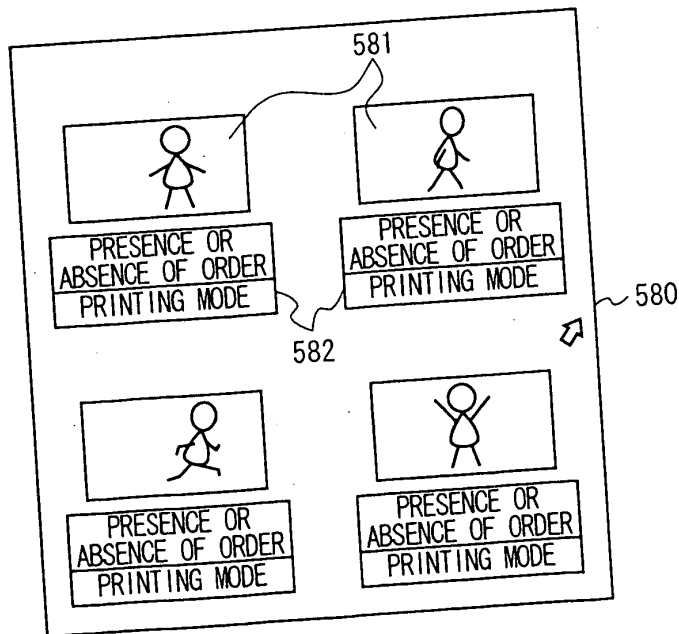


FIG.56

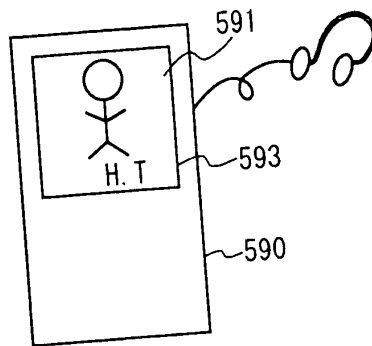


FIG. 57

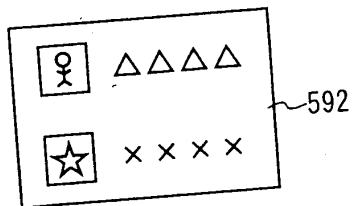


FIG. 58

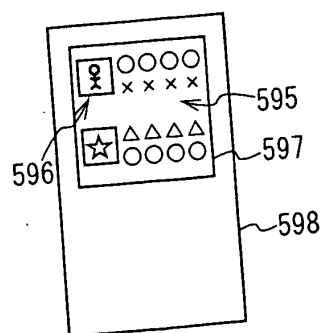


FIG. 59

FIG. 60 is a block diagram of a 601 decoder circuit (actually to be processed with software) 600. The circuit 600 is shown as a dashed box. It includes a header image generator 431, a header data separator 427, a photograph-IC image generator 433, a photograph-IC image separator 428, a photograph-IC image synthesizer 434, an intrinsic control image generator 436, an intrinsic control data separator 429, an additional control image generator 437, an additional control data separator 430, an additional data generator 435, an image character synthesizing switch 432, an image display circuit 438, a system controller 424, a key 422, and a mouse 423. The circuit 600 is connected to a film data separator 426 and a film data input 425. The circuit 600 is also connected to a film data output 425, which is labeled D85, D114.

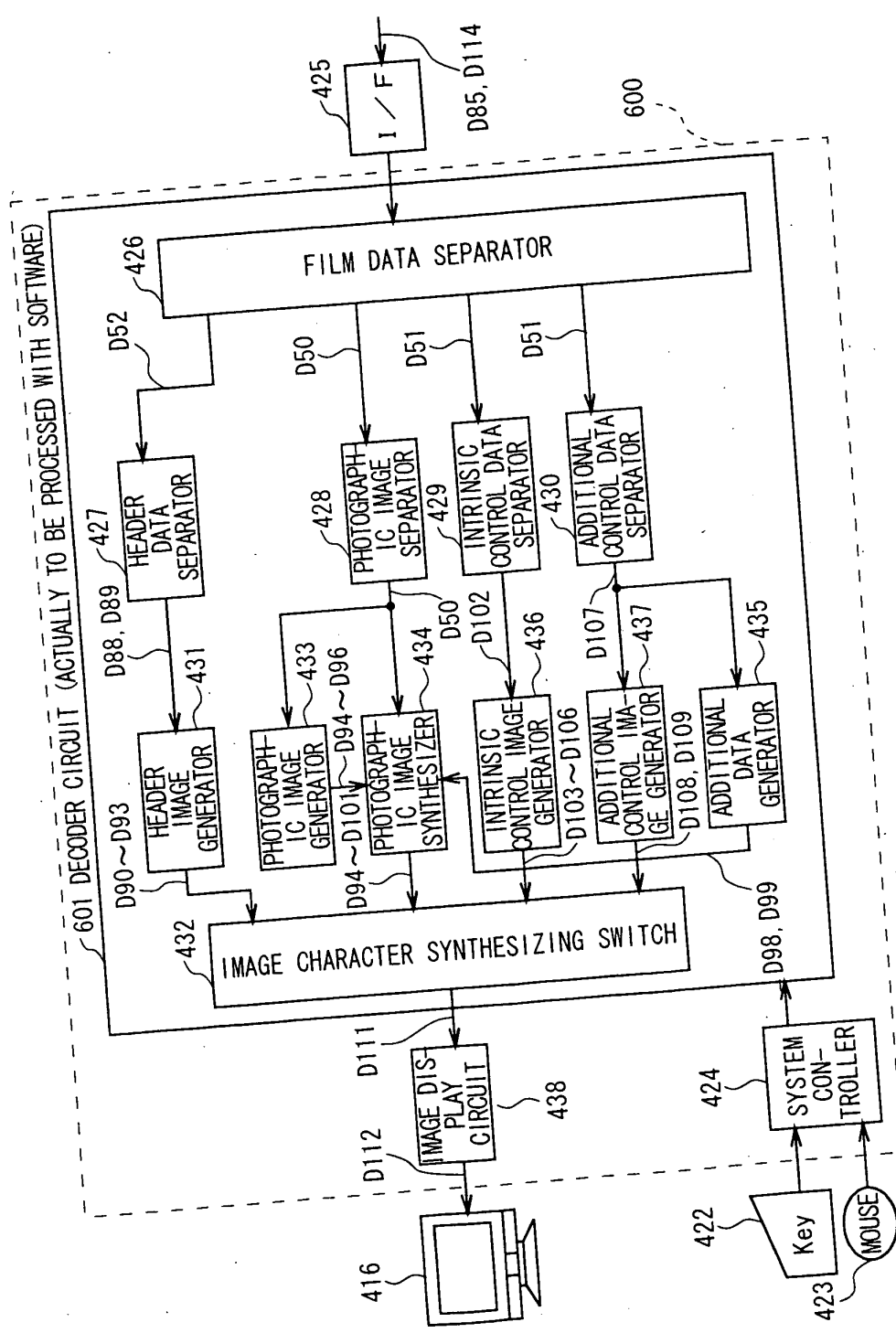


FIG.60

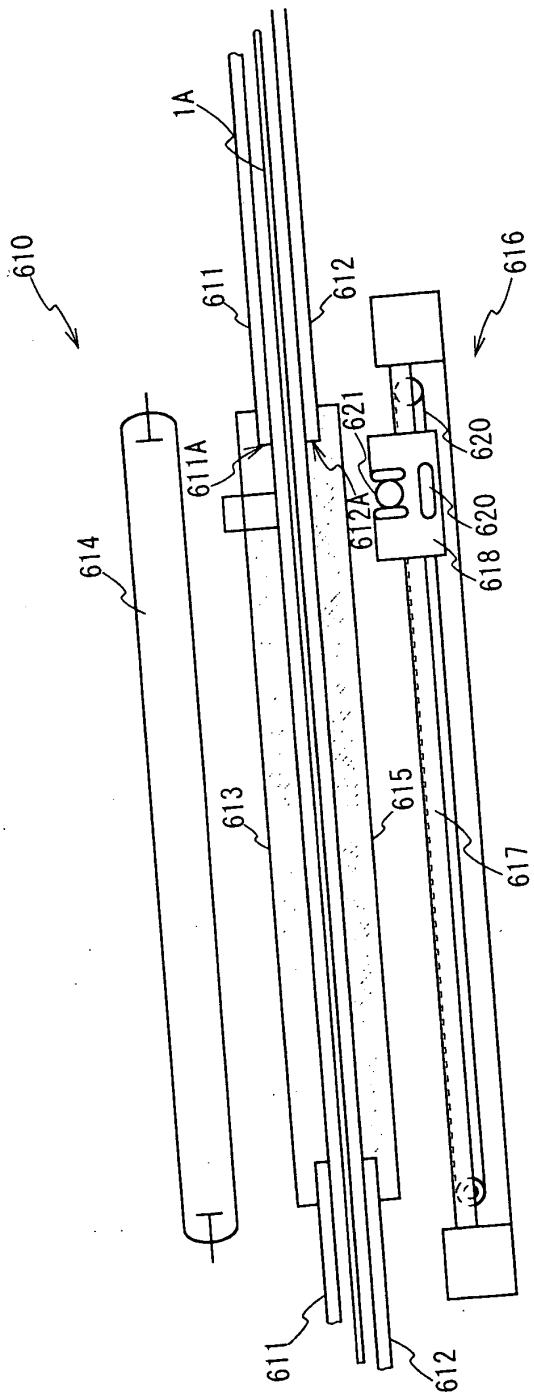


FIG. 61

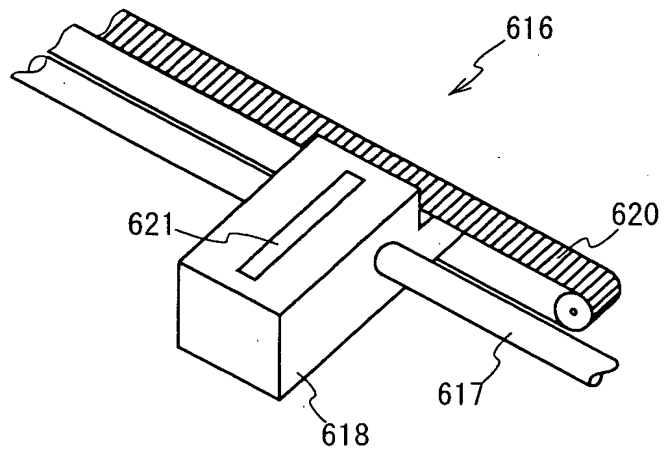


FIG. 62

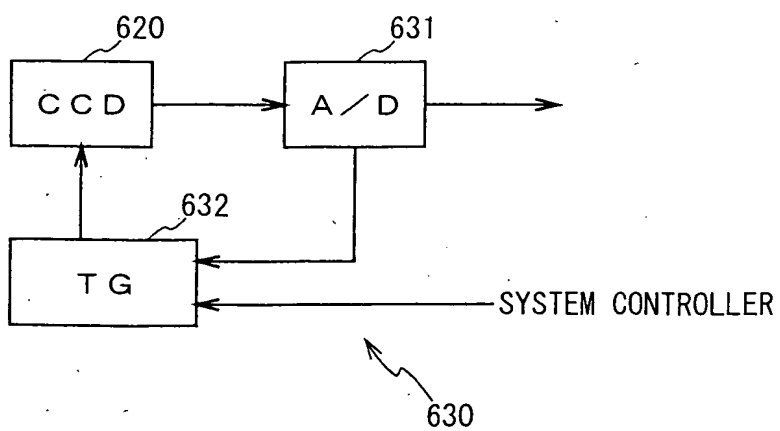


FIG. 63